ISSN: 2088-8708

Design and Analysis High Gain PHEMT LNA for Wireless Application at 5.8 GHz

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Article Info

Article history:

Received Jan 20, 2015 Revised Apr 25, 2015 Accepted May 10, 2015

Keyword:

Cascaded and Cascoded LNA IEEE 802.16 Inductive drain feedback PHEMT LNA

ABSTRACT

This research presents a design of a higher gain (66.38dB) for PHEMT LNA using an inductive drain feedback technique for wireless application at 5.8GHz. The amplifier it is implemented using PHEMT FHX76LP transistor devices. The designed circuit is simulated with Ansoft Designer SV. The LNA was designed using T-network as a matching technique was used at the input and output terminal, inductive generation to the source and an inductive drain feedback. The low noise amplifier (LNA) using lumped-component provides a noise figure 0.64 dB and a gain (S_{21}) of 68.94 dB. The output reflection (S₂₂), input reflection (S₁₁) and return loss (S₁₂) are -17.37 dB, -15.77 dB and -88.39 dB respectively. The measurement shows the stability was at 4.54 and 3-dB bandwidth of 1.72 GHz. While, the low noise amplifier (LNA) using Murata manufactured component provides a noise figure 0.60 dB and a gain (S_{21}) of 66.38 dB. The output reflection (S_{22}) , input reflection (S_{11}) and return loss (S_{12}) are -13.88 dB, -12.41 dB and -89.90 dB respectively. The measurement shows the stability was at 6.81 and 3-dB bandwidth of 1.70 GHz. The input sensitivity more than -80 dBm exceeded the standards required by IEEE 802.16.

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1. INTRODUCTION

At this point, most of the developing and advanced country used 3G technologies due to the speedy delivery of information to users at a higher bit rate. However, the bit rate of 3G technology reserve at this point is still not enough due to the high demand from consumers, especially in wireless broadband. To overcome this problem, the use of WiMAX technology was introduced to consumers to enable effective connectivity at high bit rate to a new generation of consumer devices for adaptation latest applications that are available in the market [1].

Groups of telecommunication protocol using WiMAX technology are new trademark and standards in the provision of mobile and fixed internet access. Residential construction and opening of a planned township enterprise in free zones require high data transfer rate (70 Mbps) and reach (50 km) to meet the needs of high bandwidth voice and data to support the growth of the industry [2]. WiMAX is a replacement technology for cellular phone technologies such as UMTS and GSM and can be used to increase capacity of the customer [3]. Therefore, the RF front-end receiver should be designed according to the latest specifications to support new trademark as set by the telecommunications protocols and allows it to operate in a multiple of applications on a single device.

Design of LNA amplifiers for RF receiver front-end is to obey and comply with the new standards IEEE 802.16. It to ensure that the received signal can be processed to obtain information that is transmitted without interference noise. This can only be done when the designers are able to design an RF front-end receiver with the best characteristic in the entire system such as the higher gain, low noise figure and sufficient bandwidth to accommodate the needs of the latest applications available in the market for users using WiMAX technology.

To ensure high performance signal reception to meet the standards set by IEEE 802.16, a new design in architecture receiver RF front-end should be introduced to improve the performance of existing systems. A previous researcher reported covering the extension of communication distance for the system up to 50km requires an overall gain from the range up to 50 dB [4], but to get a better performance for RF front-end receiver, we proposed for overall gain that introduce enhanced up to 65 dB.

For WiMAX standard, the system is designed to accommodate up to 200 channel subscribers while the bandwidth of the system designed is between 1600 to 1700 MHz, which is triple than the standard 20 MHz for 200 sub-carriers. In addition, the noise figure proposed by the IEEE 802.16 (WiMAX) for the RF receiver front-end architecture must be less than 3 dB. The input sensitivity of the system should cover the minimum sensitivity of -80 dBm [4].

In this paper, a new topology front end architecture using inductive drain feedback is used to achieve a gain more than 65 dB, noise figure less than 3dB and should provide bandwidth more than 1 GHz is proposed in WiMAX application. Figure 1 shows the architecture configurable for direct conversion RF front-end receiver WiMAX at 5.8 GHz is introduced. The development of combination LNA at the front-end of the receiver will be focused.

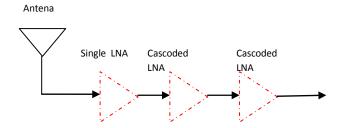


Figure 1. The new architecture for direct conversion RF front-end receiver WiMAX at 5.8 GHz

This configuration consisting of double stages cascoded LNA using inductive drain feedback combined with source inductive degeneration, inductive RF choke placed between the two LNA amplifier and the T matching network at the input and output ports. Were used inductive drain feedback at the cascoded topology has improved the gain of the LNA and will suit at matching output that it also helps in increasing the bandwidth. While the addition of an inductive source generation at cascoded LNA topology enhanced bandwidth, stability and improve input-output matching capabilities. The use of T-matching on a double stage cascoded LNA also has helped reduce the reverse isolation and noise figure.

2. LNA THEORY

Low noise amplifier (LNA) is a key factor in the improved performance of the RF front-end receiver. LNA in the WiMAX receiver application requires sufficient sensitivity to enable the receiver distinguish signal from the surrounding noise and interference to ensure that it can take an information signal sent by the transmitter. The gain, bandwidth, noise figure and linearity are the characteristic that can be controlled by the RF front-end designer that affect directly to the receiver sensitivity. Even so to control such features requires a deep understanding of the device amplifiers, active and passive components, and fabrication details to ensure the LNA amplifiers built to achieve optimal performance and only a slight tradeoff between the characteristic [5].

However, in this research, we only focused on variables such as gain, noise figure, stability, bandwidth, topology, and input and output matching for best performance of LNA amplifiers. The targeted S-parameter specification for the single LNA cascaded with double stages cascoded LNA amplifier is shown in Table 1.

Table 1. Targeted S-Parameters for a a single LNA cascaded with double stages cascoded LNA amplifier

S- parameter	Single LNA cascaded with double stages cascoded LNA <-10 dB				
	stages cascoded LNA				
Input reflection S11 (dB)	<-10 dB				
Return Loss S12 (dB)	<-10 dB				
Forward Transfer S21 (dB)	>+ 65 dB				
Output Reflection loss S22 (dB)	<-10 dB				
Noise Figure (dB)	< 3 dB				
Stability (K)	K > 1				
Bandwidth (MHz)	>1000				

2.1 Stability, Noise Figure and Power Gain

Stability is one of the important characteristics in designing LNA amplifiers. Determination of stability is essential to avoid oscillation occurs at the operating frequency. The oscillation is possible if either input or output port impedance has produced a negative real part. This would imply that $\Gamma_{in} > 1$ or $\Gamma_{out} > 1$. This because Γ_{in} and $\Gamma_{out} = 1$ on the source and the load matching network. However, the stability of the amplifier depends on Γ_s and Γ_L as presented as matching network. If low noise amplifiers is not stable, it would become useless since major properties including bandwidth, gain, noise, linearity, DC power consumption and impedance matching can be significantly degraded. In this design, a good stability was achieved (unconditionally stable) by employing the signal flow theory and S-parameter [6]. Alternatively, the amplifier will be unconditionally stable, when the stability factor (K) and delta factor (Δ) following necessary and sufficient conditions are met:

$$K = \frac{1 - |S_{11}|^2 - |S_{22}|^2 + |\Delta|^2}{2|S_{12}S_{21}|} > 1$$
 (1)

and

$$|\Delta| = |S_{11}S_{22} - S_{12}S_{21}| < 1 \tag{2}$$

 $(K \ge 1)$ and $(|\Delta| \le 1)$ is condition requirement for unconditional stability.

By using stability circle that is based on radius and center, the Table 2 below shows that the two circles need to draw both input and output.

Table 2. Input and output stability circle

Input stability circle		Output stability circle
$ \Gamma_{IN} = s_{11} + \frac{s_{12}s_{21}\Gamma_L}{1 - s_{22}\Gamma_L} = 1$		$ \Gamma_{OUT} = s_{22} + \frac{s_{12}s_{21}\Gamma_S}{1 - s_{11}\Gamma_S} = 1$
$\begin{split} \gamma_{in} &= \frac{\mathbf{S}_{12}\mathbf{S}_{21}}{ \mathbf{S}_{11} ^2 - \Delta ^2} \\ \boldsymbol{C}_{in} &= \frac{(\mathbf{S}_{11} - \Delta \mathbf{S}^*_{22})^*}{ \mathbf{S}_{11} ^2 - \Delta ^2} \end{split}$	Radius Center	$ \gamma_{out} = \left \frac{S_{12}S_{21}}{ S_{22} ^2 - \Delta ^2} \right \text{ Radius} $ $ C_{in} = \frac{(S_{22} - \Delta S^*_{11})^*}{ S_{22} ^2 - \Delta ^2} \text{ Center} $

These two circles are drawn on a Smith Chart using S-Parameter values of the FET, input matching Γ_{in} output matching Γ_{out} . Figure 2 and Figure 3 shows the input and output stability circles.

614 □ ISSN: 2088-8708

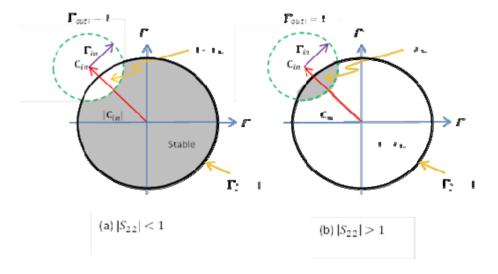


Figure 2. Output Stability Circles [7]

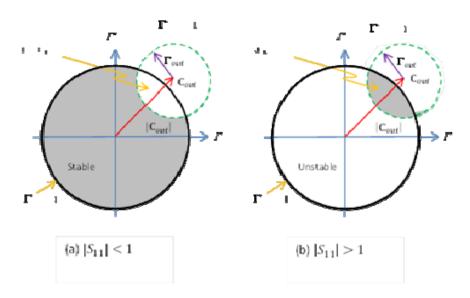


Figure 3. Input Stability Circles [7]

Noise optimization is the most critical step procedure in the LNA design. The best way to make the balance optimization of noise figure and gain using constant gain circles and circles of constant noise figure. 2-port transistor has a minimum value of the noise figure at the specified admittance given by the equation (3), [8]:

$$F = F_{\min} + \frac{R_N}{G_S} |Y_s - Y_{opt}|^2$$
 (3)

For low noise transistors, manufacturers usually provide F_{min} , R_N and Y_{opt} by frequencies. N defined by the formula for desired noise figure, shown in equation (4):

$$N = \frac{|\Gamma_{s} - \Gamma_{opt}|^{2}}{1 - |\Gamma_{s}|^{2}} = \frac{F - F_{min}}{4R_{N}/Z_{0}} |1 + \Gamma_{opt}|^{2}$$
(4)

The Power gain of 2-port networks with circuit impedance or load impedance of the power amplifier are represented with scattering coefficient classified into Available Power Gain, Power Transducer Gain and Operating Power Gain [9].

Operating power gain (G_P) , is the ratio between the power delivered to the load (P_L) and the power input (P_{in}) to the network. The Operating Power Gain can be specified as an equation (5), [8]:

$$G_{P} == \frac{P_{L}}{P_{in}} = \frac{\left|S_{21}\right|^{2} \left(1 - \left|\Gamma_{L}\right|^{2}\right)}{\left(1 - \left|\Gamma_{in}\right|^{2}\right) 1 - S_{22} \Gamma_{L}\right|^{2}}$$
(5)

Available power gain (G_A) is the ratio between the power available from the network (P_{avn}) and the power available from the source (P_{avs}) as shown in equation (6), [8]:

$$G_{A} = \frac{P_{avn}}{P_{avs}} = \frac{1 - |\Gamma_{S}|^{2}}{|1 - S_{11}\Gamma_{S}|^{2}} |S_{21}|^{2} \frac{1}{|1 - S_{22}\Gamma_{L}|^{2}}$$
(6)

Transducer power gain (G_T) is the ratio between the power delivered to the load (P_L) and the power available from the source (P_{in}) as shown in equation (7), [8]:

$$G_{T} = \frac{P_{L}}{P_{in}} = \frac{|S_{21}|^{2} (1 - |\Gamma_{S}|^{2})(1 - |\Gamma_{L}|^{2})}{|(1 - S_{11}\Gamma_{S})(1 - S_{22}\Gamma_{L}) - (S_{12}S_{21}\Gamma_{S}\Gamma_{L})|^{2}}$$
(7)

3. DESIGN OF LNA CASCADED WITH DOUBLE STAGES CASCODED LNA

In this research, the three-stage LNA can be designed by cascading by using single LNA cascaded with double stage cascoded LNA as shown on Figure 4.

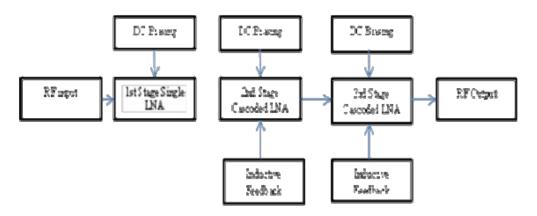


Figure 4. Three Stages LNA Block Diagram

There can be several ways to attain at the design; three different active devices (transistors) can be utilized if the same device is not able to handle the signal level in progressive stages. As the LNA under consideration is a small signal amplifier, all the three stages use the same transistor device. The inter-stage matching network is needed because this conception is wideband amplifier, and the input and output impedance of each level is matched to 50Ω . Initially the design was given VSWR's more than two at the input and output ports, merely by using the ADS optimization tools VSWR's are reduced to less than two to minimize the reflections at the input and output ports.

Figure 5 shows the complete schematic single LNA cascaded with double stage cascoded LNA using inductive feedback. The selection of the transistor is important in the design of LNA. The design of the single LNA with double stages cascoded LNA is based on the specification in Table 1. For reasonable gain and low noise figure at the required frequency requirement, the transistor used for the design of LNA is PHEMT Transistor FHX76LP. The transistor parameter at frequency 5.8 GHz are S_{11} =0.712 \bot -86.54, S_{12} =

616 □ ISSN: 2088-8708

 $0.065 \bot 33.88$, S_{21} = $8.994 \bot 178.66$ and S_{22} = $0.237 \bot -10.46$, where the parameters were obtained at V_{DD} = 2V and I_{DS} = 10mA of bias set at PHEMT.

From the S-parameters, determining the overall performance of LNA can be determined by calculating the transducer gain (GT), noise figure (NF) and the input and output standing wave ratios, VSWR_{IN} and VSWR_{OUT}. The optimum, $\Gamma_{\rm opt}$ and $\Gamma_{\rm L}$ were obtained as $\Gamma_{\rm opt} = 21 + \rm j48.02$ and $\Gamma_{\rm L} = 79.90 - \rm j7.299$ for cascoded LNA. While, $\Gamma_{\rm opt} = 18.41 + \rm j50.12$ and $\Gamma_{\rm L} = 79.913 - \rm j7.304$ for a single LNA.

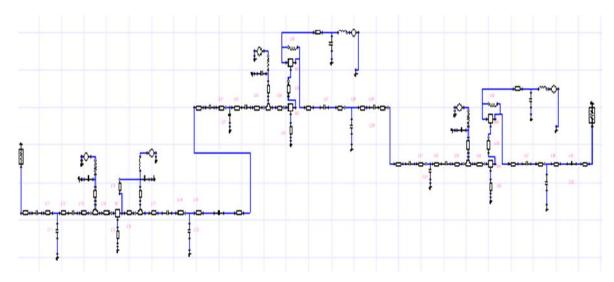


Figure 5. The complete schematic LNA cascaded with double stage cascoded LNA using inductive feedback

In this configuration, it combines LNA at the first stage, then use cascoded LNA with inductive feedback at the drain on the second and third stage. The proposed LNA design is based on a source degenerated topology (L_{10}), inductive shunt peaking at the drain (L_{15}) and T-matching network at the input and output impedance (input impedance matching at L_{11} , L_{12} , C_{11} , and output impedance matching at L_{18} , L_{19} , C_{12}). While the double stages cascoded LNA topology using latest techniques consisting of inductive feedback (L_{26} and L_{36}) are at drain M_2 and M_4 , inductive generation source (L_{20} and L_{30}) connected to the source of the M_3 and M_5 . In Addition, there L_{25} and L_{35} inductive RF choke were placed between the source drain on the M_2 and M_3 , and the source drain on the M_4 and M_5 respectively. This topology also used the T-matching network at the input and output impedance (input impedance matching component at L_{21} , L_{22} , L_{31} , L_{32} , C_{21} and C_{31} and output impedance matching component at L_{28} , L_{29} , L_{38} , L_{39} , C_{22} and C_{32}). By using Ansoft Designer SV, Smith Chart matching technique, the lumped-components for the amplifier are shown in Table 3. While the LNA amplifier for Murata-component shown in Table 4.

Table 3. LNA Amplifier parameters with Lumped-Component

		1 41	010 J. L.	12 1 2 1111p	mici pui	uniteters	With Lu	inped Co	mponer	11		
]	Lumped-C	omponents	S				
1 st Stage LNA	L ₁₀ (nH)	L ₁₁ (nH)	L ₁₂ (nH)	L ₁₃ (nH)	L ₁₄ (nH)	L ₁₅ (nH)	L ₁₆ (nH)	L ₁₇ (nH)	L ₁₈ (nH)	L ₁₉ (nH)	C ₁₁ (pF)	C ₁₂ (pF)
Value 2 nd Stage Cascoded LNA	0.078 L ₂₀ (nH)	1.346 L ₂₁ (nH)	1.371 L ₂₂ (nH)	0.449 L ₂₃ (nH)	0.439 L ₂₄ (nH)	1.271 L ₂₅ (nH)	0.445 L ₂₆ (nH)	1.366 L ₂₇ (nH)	1.195 L ₂₈ (nH)	1.368 L ₂₉ (nH)	0.264 C ₂₁ (pF)	0.010 C ₂₂ (pF)
Value 3 rd Cascoded LNA	0.064 L ₃₀ (nH)	1.346 L ₃₁ (nH)	1.016 L ₃₂ (nH)	0.698 L ₃₃ (nH)	0.367 L ₃₄ (nH)	1.159 L ₃₅ (nH)	9.000 L ₃₆ (nH)	1.367 L ₃₇ (nH)	0.658 L ₃₈ (nH)	1.369 L ₃₉ (nH)	0.100 C ₃₁ (pF)	0.600 C ₃₂ (pF)
Value	0.084	1.318	1.278	0.658	0.283	1.139	9.560	1.368	0.658	0.228	0.500	0.750

					•	•	Murata-Co	omponents		•			
1 st LNA	Stage	L ₁₀ (nH)	L ₁₁ (nH)	L ₁₂ (nH)	L ₁₃ (nH)	L ₁₄ (nH)	L ₁₅ (nH)	L ₁₆ (nH)	L ₁₇ (nH)	L ₁₈ (nH)	L ₁₉ (nH)	C ₁₁ (pF)	C ₁₂ (pF)
Value 2 nd Cascod LNA	Stage ed	0.078 L ₂₀ (nH)	1.346 L ₂₁ (nH)	1.372 L ₂₂ (nH)	0.449 L ₂₃ (nH)	0.440 L ₂₄ (nH)	1.270 L ₂₅ (nH)	0.445 L ₂₆ (nH)	1.366 L ₂₇ (nH)	1.196 L ₂₈ (nH)	1.368 L ₂₉ (nH)	0.1 C ₂₁ (pF)	0.3 C ₂₂ (pF)
Value 3 rd Ca LNA	scoded	0.077 L ₃₀ (nH)	1.345 L ₃₁ (nH)	1.016 L ₃₂ (nH)	0.676 L ₃₃ (nH)	0.3669 L ₃₄ (nH)	1.159 L ₃₅ (nH)	1.365 L ₃₆ (nH)	0.053 L ₃₇ (nH)	1.112 L ₃₈ (nH)	1.369 L ₃₉ (nH)	0.3 C ₃₁ (pF)	0.5 C ₃₂ (pF)
Value		0.081	0.227	0.705	0.657	0.365	1.138	1.372	0.658	0.225	0.220	0.3	0.7

4. RESULTS

Based on Figure 6 shows the s-parameters output of the lumped-component in used in the topology design. The Gain at 5.8 GHz arising from consumption of lumped-component in this topology was 68.94 dB. The gain is found to be nearly same as was calculated during the design procedure. In addition, the use of lumped - components of this topology has produced a 3-dB bandwidth of 1.72 GHz. The measured return loss S_{12} is -88.39 dB while the output reflection loss S_{22} is -17.37 dB, and input reflection S_{11} is – 15.77 dB. While Figure 7 shown, the stability factor obtained after matching load is 4.54 at 5.8 GHz frequency. The value of stability obtained is greater than 1, and the LNA amplifiers are currently in a state of unconditionally stable. Figure 8 shown noise figure of 0.64 dB over the band is achieved. Thus, these values achieved the design specification, as stated in Table 2.

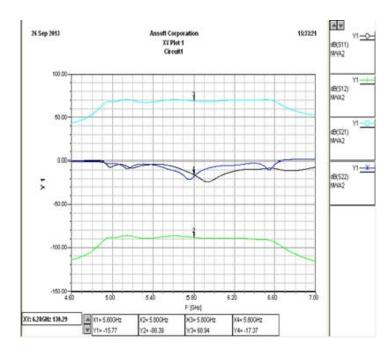


Figure 6. S-parameter for LNA Cascaded with Double Stages Cascoded LNA using the lumped - component

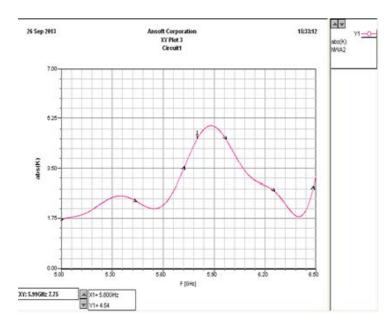


Figure 7. Stability for LNA Cascaded with Double Stages Cascoded LNA using the lumped - component

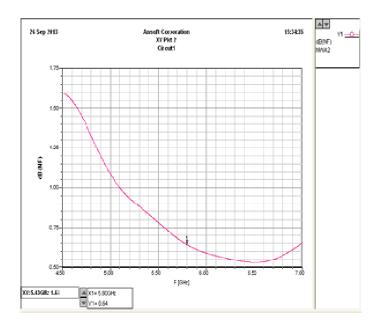
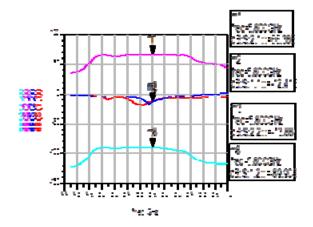


Figure 8. Noise Figure for LNA Cascaded with Double Stages Cascoded LNA using the lumped - component

Figure 9 is s-parameter output by using Murata manufactured component on the Y-Axis and frequency on the X-Axis. The low noise amplifier (LNA) using Murata manufactured component provides the gain S_{21} of 66.38 dB. The output reflection S_{22} , input reflection S_{11} and return loss S_{12} are -13.88 dB, -12.41 dB and -89.90 dB respectively. In addition, the use of this topology has produced a 3-dB bandwidth of 1.70 GHz. The gain is found to be nearly same as lumped-component during the design procedure. Another parameter that dictates the performance of the low noise amplifier is the noise figure. The noise figure of 0.60 dB as shown in Figure 10 is obtained at 5.8 GHz that also satisfies the design requirements of below than 3dB. The Voltage Standing Wave Ratio (VSWR) at the input and output shown as Figure 11 and 12. The VSWR_{IN} and VSWR_{OUT} curves are showing the input and output Voltage Standing Wave Ratio respectively. Both the standing wave ratios are found to be same as were calculated during the design procedure. This factor is important as it specifies phenomenon of standing waves due to reflections on the transmission line. Initially the design was given VSWR_{IN} and VSWR_{OUT} more than two at the input and output ports, but by

using the ADS optimization tools VSWR's are reduced to less than two to minimize the reflections at the input and output ports.



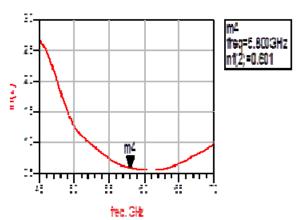
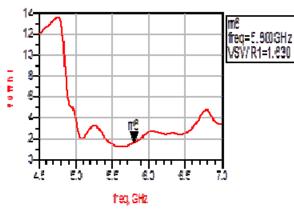


Figure 9. S-parameter for LNA Cascaded with Double Stages Cascoded LNA using Murata manufactured component

Figure 10. Noise Figure for LNA Cascaded with Double Stages Cascoded LNA using Murata manufactured component



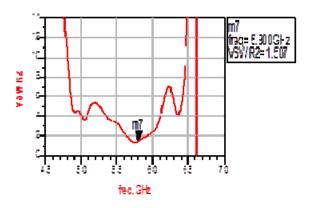


Figure 11. Input VSWR for LNA Cascaded with Double Stages Cascoded LNA using Murata manufactured component

Figure 12. Output VSWR for LNA Cascaded with Double Stages Cascoded LNA using Murata manufactured component

Table 5 shows the s-parameters output for comparison of between lumped-component and Murata manufactured component by using topology single LNA cascaded with double cascoded LNA by using the inductive drain feedback. From this comparison, we find this topology has resulted in improved performance in gain, noise figure, and bandwidth. In a variable gain performance improvements can be tuned to reach to gain as a lumped - component at 68.94 dB. Meanwhile, there was a significant reduction on noise figure of 0.64 dB to 0.60 dB. Table 6 shows the comparison of recently reported LNA.

Table 5. The s-parameters output for comparison of between lumped-component and Murata manufactured

		component	
Parameter	Targeted	Lumped-Component	Murata Manufactured Component
Input Reflection S ₁₁ dB	< -10	-15.77	-12.41
Output Reflection S22 dB	< -10	-17.37	-13.88
Forward transfer S21 dB	>+ 65	68.94	66.38
Return Loss S ₁₂ dB	<-10	-88.39	-89.90
NF dB	< 3	0.64	0.60
BW GHz	1	1.72	1.70
Stability (K)	K > 1	4.54	6.81

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Table 6.	Comparison	of recently	v LNAs
I do I o .	Companion	OI ICCCIICI	,

S- parameter	This work	(Sobhy et al., 2011)	(Karpagam and Sampath, 2013)
Topology	Single LNA Cascaded with	CGLNA with multiple	Differential LNA
	Double Stages Cascoded LNA	feedback	
Input Reflection S ₁₁ dB	-12.41	<-10	-15.075
Output Reflection S22 dB	-13.88	<-10	-
Forward transfer S ₂₁ dB	66.38	23	25.07
Return Loss S ₁₂ dB	-89.90	-	-
NF dB	0.60	2	1.07
BW GHz	1.70	1.70	-
Stability (K)	6.81	>1	1.12

5. CONCLUSION

The main aim was to design and analyze new topology using an inductive drain feedback implemented in PHEMT technology at 5.8 GHz. The important parameters related to microwave amplifiers are described, including gain and noise figure circles. Trade-off between noise figure and gain is also considered in point for a low noise amplifier. Obtained from the proposed topology allows the designer to control LNA variable performance, such as noise figure, bandwidth, gain and stability in the LNA circuit Recorded result for amplifier obtained the noise figure (NF) of 0.60 dB and the gain (S₂₁) of 66.38 dB. While the stability (K) is 6.81 and 3-dB bandwidth is 1.70 GHz. LNA performance can be further enhanced by strengthening input and output impedance matching of the output reflection loss (S₂₂), input reflection loss (S₁₁) and return loss (S₁₂) of the respective value are -13.88 dB, -12.41 dB and -89.90 dB. In conclusion, use of this topology has improved performance of the LNA amplifiers in RF receiver mainly on noise figure, gain, bandwidth, and stability.

ACKNOWLEDGMENT

The work described in this paper was fully supported by **Centre For Research And Innovation Management** (**CRIM**), Universiti Teknikal Malaysia Melaka (UTeM). Melaka, Malaysia, under research grant PJP/2013/FKEKK (11C)/S01182.

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