Design of a novel three phase hybrid converter for microgrids application using renewable energy sources

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Article Info

Article history:

Received Jul 22, 2024 Revised Nov 5, 2024 Accepted Nov 20, 2024

Keywords:

Hybrid H-bridge inverter Multi-level inverter Pulse width-modulation Renewable energy sources Total harmonic distortion

ABSTRACT

A multi-level inverter (MLI) plays a vital role in recent days with an increasing trend of usage of microgrid and distributed generator. MLIs are popular in high voltage and high-power applications. MLIs operates with dominant switching frequency pulse width-modulation (PWM) techniques. These MLIs not only generate the output voltage with fewer Harmonic but also reduces the dV/dt stress on switches. The induction machine connected to these MLIs, adds greater advantages in real time applications. This paper presents a novel 13 and 21-level hybrid H-bridge inverter (HHBI) connected to induction drive by using a photovoltaic module for microgrid applications using maximum power point tracking (MPPT) through a PV array. Hybrid H-bridge inverters combine elements from different inverter topologies to optimize appearance in terms of efficiency, harmonics and system complexity. The main aim is to reduce harmonics using high level of inverter and by controlling motor characteristics. Here a novel PWM control method is used for making the exchanging sequences for the corresponding switches. From the MATLAB results presented, it can be noticed that with the proposed methodology the THD is reduced to 4.66 and number of switches to 39, which reduces the complexity of the system. It also minimized the switching losses and increases efficiency.

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1. INTRODUCTION

Multi-level inverters are playing a vital role in recent day's applications with an increasing usage of renewable energy sources [1]–[3]. These inverters are also helpful in micro grid applications. There are two levels of inverter with voltages +V and -V, these two levels of voltage changed from pulse width-modulation (PWM) technique strategy make successful harmonics distortion, EMI and dV/dt stress [4], [5].

Various multilevel inverter topologies given in [6]–[9]. Major multi-level inverter (MLI) topologies with cascaded H-bridges are considered in studies [10], [11]. Each of these configurations utilizes distinct approaches for voltage modulation, employing either separate DC sources, diode clamps, or flying capacitors for achieving multilevel output waveforms. Multi-level inverter incorporates several ideal power switches with a combination of capacitor and flying diode, expanding the quantity of voltage levels [12]. Cascaded hybrid multi-inverters are planned with a different direct current (DC) voltage source and are productive to utilize when contrasted. Since there is no need of capacitors and diodes here and it additionally utilizes distinctive unbalanced voltage sources from renewable energy sources like fuel cell, sunlight-based cell and

wind energy [13]. More voltage levels are possible to accomplished by inserting more H-bridges which produces smooth sinusoidal waveform with a lower total harmonic distortion (THD) proportion [14], [15].

The novel inverter topology provides a significant advancement in multilevel inverter technology for solar PV systems [9]. It offers a promising solution for improving the cost-effectiveness and performance of renewable energy systems. The inverter can fill in as a three-stage direct current (DC) to alternating current (AC) inverter, AC to AC inverter, and it can use system and PV power directly [16], [17]. Explores symphonious concealment, PWM systems, and their benefits and drawbacks. It also discusses various PWM methods, their advantages and disadvantages, and future projections. The development of hybrid micro-grids for residential use that integrate the AC energy grid, electric car charging stations, and renewable energy sources. New solar inverters enable local energy storage for renewable energy sources with this model [18].

Renewable energy-based microgrids, such as AC and DC, are effective in addressing energy crises and environmental issues. DC-dominated hybrid microgrids combine advantages of both types, but require proper power coordination control for stable operation [19]–[22]. Discusses commonly applied control techniques for hybrid microgrids. A model predictive control (MPC)-based distributed control algorithm is given in [23] for hybrid AC/DC microgrids. It analyzes grid conditions and designates BIC to regulate the highest deviated parameter in nonlinear conditions. The study in [24] presents a transformer-less DC-DC power boost converter with a switched-capacitor structure and sliding mode controller (SMC) for PV applications. The converter increases DC voltage gain and reduces voltage stress on the power switch, allowing longer off-time for continuous current mode operations. The converter's control process is simpler compared to multi-switched topologies.

The Arvin converter given in [25] offers numerous advantages, including a common ground-based high-gain topology, a simple fuzzy logic controller, bidirectional energy flow, cost-effectiveness, and a reduced number of semiconductor devices and passive components. A multi cell hybrid 21-level multilevel inverter is given in [26], [27] with a two-unit topology. The topology generates high output efficiency, low distortion, and reduced electromagnetic interface (EMI) levels. The topology also offers an online method for charging and balancing capacitors without auxiliary circuits. The pulse-width-modulation signals of the 3-level hybrid boost converter based on a single-phase 3-level T-type inverter in [28], [29] is created by comparing two duty cycles and two triangular carrier signals. The converter contains four power switches, four diodes, an inductor, and two capacitors [30]–[32]. Discusses a novel configuration for high gain, high efficiency DC-DC converters that include a single switch, two intermediate capacitors, and a linked inductor for low voltage solar PV module supplied applications. The circuit's structure is designed to lessen the voltage stress on the power device, enhancing efficiency. The proposed work contributes to the field of hybrid converters by incorporating renewable energy sources. Moreover, the efficacy of the proposed system is rigorously validated through simulations conducted in the MATLAB environment.

2. MICROGRID

A micro grid is an amalgamation of at least two environmentally friendly power sources (*i.e.*, solar, wind or fuel cell) which is used to supply the power demand of the consumer. It can be directly used to supply the power to the consumer or it can be used as the secondary power source for the main grid. A micro grid can control itself by using some control strategies such as PQ control, droop control, peer control and decentralized control. Micro grid collects energy from various DG's and supplies that energy to the consumer according to the demand. A DG generates electrical energy, and the generated electrical energy is not transmitted for the longer distances, like solar, wind and small generator that runs on diesel or petrol is used as a distributed generator. There are various challenges associated with integrating distributed energy sources and improving grid stability. Applying multilevel inverters in grid-connected systems overcomes above challenges. Multilevel inverters reduce THD and enhances grid support by generating voltages in multiple levels, reduces filtering requirements, and provides flexibility and scalability.

2.1. Classification of microgrid

Micro grids are grouped into four types in particular. The four types are institutional micro grid, community microgrid, military grade micro grid, and remote off-grid network. Which are described as follows:

- a. Institutional micro grid: the onsite distributive generators are integrated and are controlled by the owner easily in a small area.
- b. Community micro grid: there are 1000s of consumers in this type of micro grid, some of the consumers may have renewable energy sources that are used by them and they generate some excessive amount and that generated excessive amount is being given to the other users who are in need.
- c. Military grade micro grid: used for military activities and these does not depend on the normal grid, this work independently all the time.

d. Remote off-grid micro grid: this is islanded mode of operation. These types of micro grids are employed where transmission of power from a longer distance is not possible. These work individually from macro grid.

2.2. Induction motor

The induction drive is very comfortable for agricultural modeling in micro grid application because of its high discharge. A DC motor circuit consists of commutator and brushes so that there is a scope of getting frequent sparks, with this reason, the DC motors are normally not preferred in agricultural applications. Because of these reasons generally induction motors are preferred for speed regulated industrial drive applications. Utilizing an induction drive has a few specialized troubles too on the grounds that it requires two supply transformation steps, AC-DC and DC-AC conversions play a critical role in variable-frequency (V/F) control setups. They entail converting AC to DC and vice versa, frequently utilized for motor speed regulation and maintaining specific voltage-to-frequency ratios. Because of its superior electrical properties over different arrangements, turning around voltage topologies with 13 and 21 level converters are regularly utilized in induction drive.

2.3. Induction motor dynamic modeling

For a four-pole machine, the induction drive dynamic modeling comprises of two similar voltage windings. The connections between these windings are antiparallel. These two windings are cut off for the proposed hybrid converter, leaving only the four available terminals. Because of the equivalent dividing between these two windings, the stator resistance, inductance and magnetizing inductance of each similar voltage winding are each equipped for 1/2 the typical voltage value displayed in Figure 1 the accompanying conditions simplify it to analyze the dynamic modeling of a drive.

$$V_{a1} - V_{a2} = \left(\frac{r_s}{2}\right) * i_{as} + \left(\frac{L_{ss}}{2}\right) * i_{as} - \left(\frac{1}{2}\right) * \left(\frac{L_m}{2}\right) * i_{bs} - \left(\frac{1}{2}\right) * \left(\frac{L_m}{2}\right) * i_{cs}$$
(1)

$$V_{a3} - V_{a4} = {\binom{r_s}{2}} * i_{as} + {\binom{L_{ss}}{2}} * i_{as} - {\binom{1}{2}} * {\binom{L_m}{2}} * i_{bs} - {\binom{1}{2}} * {\binom{L_m}{2}} * i_{cs}$$
(2)

The resultant equivalent voltage is algebraic summation of voltages of (1) and (2).

$$V_{as} = (V_{a1} - V_{a2}) + (V_{a3} - V_{a4})$$
(3)

The per phase voltage of drive can be calculated as (4).

$$V_{as} = r_s * L_{ss} + i_{as} * -\left(\frac{1}{2}\right) * L_m * i_{bs} - \left(\frac{1}{2}\right) * L_m * i_{cs}$$
(4)



Figure 1. Equivalent (a) stator winding and (b) antiparallel stator winding

In the above method, voltages for other phases can be calculated as (5), (6).

$$V_{bs} = r_s * i_{bs} + L_{ss} * i_{bs} - \left(\frac{1}{2}\right) * L_m * i_{as} - \left(\frac{1}{2}\right) * L_m * i_{cs}$$
(5)

$$V_{cs} = r_s * i_{cs} + L_{ss} * i_{cs} - \left(\frac{1}{2}\right) * L_m * i_{as} - \left(\frac{1}{2}\right) * L_m * i_{bs}$$
(6)

Voltage conditions in dq0 outline is acquired from the overall conditions of induction drive.

$$V_{qs} = r_s * i_{qs} + \omega * \lambda_{ds} + \rho * \lambda_{qs}$$
⁽⁷⁾

$$V_{ds} = r_s * i_{ds} - \omega * \lambda_{qs} + \rho * \lambda_{ds}$$
(8)

$$V_{0s} = r_s * i_{0s} + \rho * \lambda_{0s} \tag{9}$$

$$V_{qr} = r_r * i_{qr} + (\omega - \omega_r) * \lambda_{dr} + \rho * \lambda_{qr}$$
⁽¹⁰⁾

$$V_{dr} = r_r * i_{dr} - (\omega - \omega_r) * \lambda_{qr} + \rho * \lambda_{dr}$$
⁽¹¹⁾

$$V_{0r} = r_r * i_{0r} + \rho * \lambda_{0r} \tag{12}$$

Flux linkages are

$$\lambda_{qs} = L_{ss} * i_{qs} + L_m * i_{qr} \tag{13}$$

$$\lambda_{ds} = L_{ss} * i_{ds} + L_m * i_{dr} \tag{14}$$

$$\lambda_{0s} = L_{1s} * i_{0s} \tag{15}$$

$$\lambda_{qr} = L_{rr} * i_{qr} + L_m * i_{qs} \tag{16}$$

$$\lambda_{dr} = L_{rr} * i_{dr} + L_m * i_{ds} \tag{17}$$

$$\lambda_{0r} = L_{1r} * i_{0r} \tag{18}$$

Torque (T_e) as far as dq0 current flows is

$$T_e = \left(\frac{3}{2}\right) * \left(\frac{P}{2}\right) * L_m * (i_{qs} * i_{dr} + i_{ds} * i_{qr})$$
(19)

Speed of rotor is

$$\frac{d}{dt}\omega_e = \left(\frac{P}{2*J}\right) * \left(T_e - T_L\right) \tag{20}$$

Wherever, d: direct-axis, q: quadrature-axis, s: stator indices, r: rotor indices, P: number of poles, J: moment based on inertia, T_e : electrical torque, T_L : torque. V_{qs} , V_{ds} : stator voltages along the q and d-axes, V_{qr} , V_{dr} : rotor voltages along the q and d-axes, r_r , L_{1r} : rotor resistance, leakage inductance, V_r , L_{1s} : stator currents along the q and d-axes, and i_{qr} , i_{dr} : Rotor currents along the q and d-axes.

The block executes conditions based on a fixed rotor reference (dq) frame. The d-axis coincides with the a-axis. All quantities in the rotor reference frame refer to the stator as represented in Figure 2. These employs the conditions to calculate the electrical speed (ωem) and slip speed ($\omega slip$).

$$\omega em = P\omega m \ \omega slip = \omega syn - \omega em$$

Directed toward compute the dq rotor speed regarding the rotor A-hub (dA), the block utilizes the distinction among the stator a-axis (da) speed and slip speed:

$$\omega dA = \omega da - \omega em \tag{21}$$

To improve on the situations for the flux, voltage, and current changes, the block utilizes a fixed note outline:

$$\omega da = 0\omega dA = -\omega em \tag{22}$$

To improve on the situations for flux, v and I outline:

 $\omega da = 0\omega dA = -\omega em$

The flux equations can be describing as,

$$\frac{d}{dt} \begin{bmatrix} \lambda_{sd} \\ \lambda_{sd} \end{bmatrix} = \begin{bmatrix} V_{sd} \\ V_{sd} \end{bmatrix} - R_S \begin{bmatrix} i_{sd} \\ i_{sq} \end{bmatrix} - W_{da} \begin{bmatrix} 0 & -1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \lambda_{sd} \\ \lambda_{sd} \end{bmatrix}$$
$$\frac{d}{dt} \begin{bmatrix} \lambda_{rd} \\ \lambda_{rd} \end{bmatrix} = \begin{bmatrix} V_{rd} \\ V_{rd} \end{bmatrix} - R_r \begin{bmatrix} i_{rd} \\ i_{rd} \end{bmatrix} - W_{dA} \begin{bmatrix} 0 & -1 \\ 1 & 0 \end{bmatrix} \begin{bmatrix} \lambda_{rd} \\ \lambda_{rd} \end{bmatrix}$$
$$\begin{bmatrix} \lambda_{sd} \\ \lambda_{sq} \\ \lambda_{rd} \\ \lambda_{rd} \end{bmatrix} = \begin{bmatrix} Ls & 0 & Lm & 0 \\ 0 & Ls & 0 & Lm \\ Lm & 0 & Lr & 0 \\ 0 & Lm & 0 & Lr \end{bmatrix} \begin{bmatrix} i_{sd} \\ i_{sq} \\ i_{rd} \\ i_{rg} \end{bmatrix}$$

Current is given by:

$$\begin{bmatrix} i_{sd} \\ i_{sq} \\ i_{rd} \\ i_{rq} \end{bmatrix} = \left(\frac{1}{L_m^2 - LrLs}\right) \begin{bmatrix} -Ls & 0 & Lm & 0 \\ 0 & -Ls & 0 & Lm \\ Lm & 0 & -Lr & 0 \\ 0 & Lm & 0 & -Lr \end{bmatrix} \begin{bmatrix} \lambda sd \\ \lambda sq \\ \lambda rd \\ \lambda rd \\ \lambda rq \end{bmatrix}$$

Inductance is given by

$$Ls = Lls + Lm; Lr = Llr + Lm$$

Power constant dq change to guarantee that the dq and 3-stage powers are equivalent and the corresponding voltage equations are given by

$$\begin{bmatrix} V_{sd} \\ V_{sq} \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos(\theta_{da}) & \cos(\theta_{da} - \frac{2\pi}{3}) & \cos(\theta_{da} + \frac{2\pi}{3}) \\ -\sin(\theta_{da}) & -\sin(\theta_{da} - \frac{2\pi}{3}) & -\sin(\theta_{da} + \frac{2\pi}{3}) \end{bmatrix} \begin{bmatrix} Va \\ Vb \\ Vc \end{bmatrix}$$
$$\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = \sqrt{\frac{2}{3}} \begin{bmatrix} \cos(\theta_{da}) & -\sin(\theta_{da}) \\ \cos(\theta_{da} - \frac{2\pi}{3}) & -\sin(\theta_{da} - \frac{2\pi}{3}) \\ \cos(\theta_{da} + \frac{2\pi}{3}) & -\sin(\theta_{da} + \frac{2\pi}{3}) \end{bmatrix} \begin{bmatrix} i_{sd} \\ i_{sq} \end{bmatrix}$$



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3. SYSTEM BLOCK DIAGRAM OF A HYBRID INVERTER CONNECTED TO INDUCTION DRIVE

The block diagram shown in Figure 3 illustrates the system representation for a novel photovoltaic (PV) based single-stage 13-level and 21-level hybrid H-bridge inverter (HHBI). It is connected to an induction motor. Here, the input of the induction motor is linked to the output of an inverter by using smoothing reactors.



Figure 3. System block diagram for a novel PV hybrid inverter fed to motor drive

3.1. 13-level novel hybrid inverter topology

Figure 4 illustrates the circuit topology of a novel PV-based 13-level hybrid inverter. The corresponding rearrangement sequence is detailed in Table 1 and modes of exchanging voltage from 0 to V_{dc} in Table 2. The switching operations of 13-level hybrid inverter are given in Figure 5, with voltage level '0' in Figure 5(a) and voltage level $V_{dc}/6$ in Figure 5(b) for implementation of topology.



Figure 4. Novel PV based hybrid inverter topology

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Table 1. Working sequence of an inverter									
Voltage level	S1	S2	S 3	S4	S5	S6	S 7	S 8	S9
0	0	1	0	1	0	0	0	0	0
$V_{DC}/6$	0	1	0	0	0	0	0	0	1
$V_{DC}/3$	0	1	0	0	0	0	0	1	0
$V_{DC}/2$	0	1	0	0	0	0	1	0	0
$2V_{DC}/3$	0	1	0	0	0	1	0	0	0
$5V_{DC}/6$	0	1	0	0	1	0	0	0	0
V _{DC}	1	1	0	0	0	0	0	0	0
$-V_{DC}/6$	0	0	1	0	1	0	0	0	0
$-V_{DC}/3$	0	0	1	0	0	1	0	0	0
$-V_{DC}/2$	0	0	1	0	0	0	1	0	0
-2V _{DC} /3	0	0	1	0	0	0	0	1	0
$-5V_{DC}/6$	0	0	1	0	0	0	0	0	1
-Vdc	1	0	1	0	0	0	0	0	0

Table 2. Gives the modes of exchanging voltage from 0 to V_{dc}

S. No	Voltage level	Current direction
1	0	S4-A-B-S2
2	$V_{DC}/6$	$V_{DC}/6^+-D_{17}-S09-D_{18}-A-B-S2-V_{DC}/6^-$
3	$V_{DC}/3$	$V_{DC}/6^+-D_{13}-S08-D_{14}-A-B-S2-V_{DC}/6^-$
4	$V_{DC}/2$	$V_{DC}/6^+-D_{09}-S07-D_{10}-A-B-S2-V_{DC}/6^-$
5	$2V_{DC}/3$	$V_{DC}/6^+-D_{05}-S06-D_{06}-A-B-S2-V_{DC}/6^-$
6	$5V_{DC}/6$	$V_{DC}/6^+-D_{01}-S05-D_{02}-A-B-S2-V_{DC}/6^-$
7	V_{DC}	A-S1-B-S2



Figure 5. Switching operations of 13 level hybrid inverter with (a) '0' voltage level and (b) $V_{DC}/6$ voltage level

3.2. Simulink wave forms of a novel PV based 3-phase 13-level HHBI fed to induction drive

Figures 6 to 10 represent Simulink wave forms of a novel PV based 3-phase 13-level HHBI fed to induction drive. It can be observed from above wave forms that with 13-level HHBI the THD has been reduced to 7.41% and the voltage, current and speed wave forms reach steady state at 0.3 sec after the transients.



Figure 6. Voltage wave form of an induction drive



Figure 7. Stator current of an induction drive





Figure 8. Speed wave form

Figure 9. T-Characteristics of induction drive



Figure 10. THD of induction drive

4. PROPOSED 21-LEVEL HYBRID H-BRIDGE INVERTER

In this configuration 10 DC voltage supplies are utilized with $V_{DC}/10$. Figure 11 addresses circuit topology for 21-level HHBI and the switching activity is given in Table 3. Table 4 gives the various current paths for voltage levels 0 to V_{DC} HHBI based on the states of the converter elements. The elements which are conducted at different time intervals are presented in Table 4.

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Figure 11. 21-level hybrid inverter topology's

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Voltage level	S1	S2	S3	S4	S5	S6	S7	S 8	S9	S10	S11	S12	S13
0	0	1	0	1	0	0	0	0	0	0	0	0	0
$V_{DC}/10$	0	1	0	0	0	0	0	0	0	0	0	0	1
$2V_{DC}/10$	0	1	0	0	0	0	0	0	0	0	0	1	0
$3V_{DC}/10$	0	1	0	0	0	0	0	0	0	0	1	0	0
$4V_{DC}/10$	0	1	0	0	0	0	0	0	0	1	0	0	0
$5V_{DC}/10$	0	1	0	0	0	0	0	0	1	0	0	0	0
$6V_{DC}/10$	0	1	0	0	0	0	0	1	0	0	0	0	0
$7V_{DC}/10$	0	1	0	0	0	0	1	0	0	0	0	0	0
$8V_{DC}/10$	0	1	0	0	0	1	0	0	0	0	0	0	0
$9V_{DC}/10$	0	1	0	0	1	0	0	0	0	0	0	0	0
V_{DC}	1	1	0	0	0	0	0	0	0	0	0	0	0
$-V_{DC}/10$	0	0	1	0	1	0	0	0	0	0	0	0	0
$-2V_{DC}/10$	0	0	1	0	0	1	0	0	0	0	0	0	0
$-3V_{DC}/10$	0	0	1	0	0	0	1	0	0	0	0	0	0
$-4V_{DC}/10$	0	0	1	0	0	0	0	1	0	0	0	0	0
$-5V_{DC}/10$	0	0	1	0	0	0	0	0	1	0	0	0	0
$-6V_{DC}/10$	0	0	1	0	0	0	0	0	0	1	0	0	0
$-7V_{DC}/10$	0	0	1	0	0	0	0	0	0	0	1	0	0
$-8V_{DC}/10$	0	0	1	0	0	0	0	0	0	0	0	1	0
$-9V_{DC}/10$	0	0	1	0	0	0	0	0	0	0	0	0	1
$-V_{DC}$	0	0	1	1	0	0	0	0	0	0	0	0	0

Table 3. Switching order of a 21-level HHBI

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Tab	le 4. Vo	ltages and cu	rrent paths for different time intervals				
	S. No.	Voltage level	Current direction				
	1	0	S4-A-B-S2				
	2	V _{DC} /10	$V_{DC}/10^+-D_{33}-S13-D_{34}-A-B-S2-V_{DC}/10^-$				
	3	2 V_{DC} /10	$V_{DC}/10^+-D_{29}-S12-D_{30}-A-B-S2-V_{DC}/10^-$				
	4	3 V_{DC} /10	$V_{DC}/10^+-D_{25}-S11-D_{26}-A-B-S2-V_{DC}/10^-$				
	5	4 V_{DC} /10	$V_{DC}/10^+-D_{21}-S10-D_{22}-A-B-S2-V_{DC}/10^-$				
	6	5 V_{DC} /10	$V_{DC}/10^+-D_{17}-S09-D_{18}-A-B-S2-V_{DC}/10^-$				
	7	6 V_{DC} /10	$V_{DC}/10^+-D_{13}-S08-D_{14}-A-B-S2-V_{DC}/10^-$				
	8	7 V_{DC} /10	$V_{DC}/10^+-D_{09}-S07-D_{10}-A-B-S2-V_{DC}/10^-$				
	9	8 V_{DC} /10	$V_{DC}/10^+-D_{05}-S06-D_{06}-A-B-S2-V_{DC}/10^-$				
	10	9 V_{DC} /10	$V_{DC}/10^+-D_{01}-S05-D_{02}-A-B-S2-V_{DC}/10^-$				
	11	V_{DC}	A-S1-B-S2				

In this 10 DC voltage source, entire $V_{DC}/10$ is used. There are 13 switches in all, with just two switches in the ON position for each voltage level. The voltage levels for positive orientation are 0, $V_{DC}/10$, 2 $V_{DC}/10$, 3 $V_{DC}/10$, 4 $V_{DC}/10$, 5 $V_{DC}/10$, 6 $V_{DC}/10$, 7 $V_{DC}/10$, 8 $V_{DC}/10$, 9 $V_{DC}/10$, and V_{DC} , while corresponding voltages for negative orientations are denoted by a negative sign. The voltage change rate (dV/dt) at each level is $V_{DC}/10$. As a result, pressure on switches is reduced significantly. Figures 12 (a) and 12(b) shows the actual path for positive voltage levels of the 21-level HHBI.



Figure 12. 3-phase 21-level HHBI fed to induction drive, conduction of devices for (a) '0' voltage level and (b) V_{DC} 10 voltage level

4.1. Simulink wave forms of a novel PV based 3-phase 21-level HHBI fed to induction drive

The implemented novel PV based 3-phase 21-level HHBI fed to induction drive output waveforms are shown in Figures 13 to 17. From the outputs wave forms of 21-level HHBI, THD has been reduced to 4.66% and the resulted voltage, current and speed of the drive system are within the prescribed limits. The implemented 13 and 21-level HHDI topologies are compared on the basic of switches and THD as given in Table 5.



Figure 13. Voltage waveforms of inverter phase leg



Figure 14. 21-level o/p voltage of proposed three-stage HHBI





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Figure 16. Speed (N) of induction drive

		Table 5. Comparison of results	
S. No.	Parameter	3-phase hybrid H-bridge inverter -13	3-phase recommend hybrid H-bridge
		level [29]	inverter -21 level [26]-[28]
1	No. of switches	27	39
2	No. of DC supply voltages	18	27
3	Total THD%	7.41	4.66



Figure 17. THD for 21-level inverter

5. CONCLUSION

This paper presents a novel 3-phase 13 and 21-level Hybrid H-Bridge Inverter related with induction drive by including X/R ratio for loading condition. From the modelling equations, Simulink implementation and its results, it is evident that as the level of output voltage increases, the total THD value decreases. Simultaneously the utilization of the prerequisite of the number switches and their particular driver circuits for planning the topology is additionally increments. The novel topology of HHBI reduced the number of switches when compared with all the conventional topologies in the literature. Subsequently, H-Bridge inverter fed induction machine adds more benefits in real time applications. The number of switches are minimized to 39 and the voltage THD value is reduced to 4.66%, and the initial transients are vanished within the limits.

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