

Three-phase four-wire shunt hybrid active power filter model with model predictive control in imbalance distribution networks

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ABSTRACT

This paper presents a harmonic reduction and load imbalance model in a three-phase four-wire distribution network. This model uses a hybrid active power filter, a passive inductor and capacitor filter, and an active power filter in the form of a three-phase, four-leg connected grid inverter. The switching of the voltage source converter on this filter uses finite control set model predictive control (FCS-MPC). Control of this hybrid active power filter uses model predictive control (MPC) with a cost function, comparing the reference current and prediction current with mathematical modelling of the circuit. The reference current is taken from the load current by extracting dq, and the predicted current is obtained from the iteration of the voltage source converter (VSC) switching pattern. Each combination is compared with the reference current in the cost function to get the smallest error used as a power switching signal. Modelling was validated by using MATLAB Simulink. The simulation results prove a decrease in harmonics at a balanced load from 22.16% to 4.2% and at an unbalanced load, reducing the average harmonics to 4.74%. The simulation also decreases the load current imbalance in the distribution network. Reducing the current in the neutral wire from 62.01%-0.42% and 11.29-0.3 A.

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1. INTRODUCTION

The load balance has rarely occurred in the electricity distribution grid, as most single-phase charges are connected with different power ratings and numbers, resulting in imbalance. This further causes a current flow on the neutral wire, leading to the overheating of the grid and transformer, reducing transmission capability, and increasing transmission losses [1], [2]. The unbalanced load generates a negative sequence component current in a three-phase four-wired system and develops a zero-sequence charge in the neutral wire. The imbalance analysis has been described by various methods [3], [4], such as the Buchholz theorem based on apparent power, developed and redefined for multiple sources and unbalanced power load conditions. This apparent power is defined by IEEE standard 2010, as the root of quadratic active power addition, with reactive and unbalanced watts [5].

Moreover, the load imbalance is reported to be corrected by using a compensator. This improvement is compensated for the grid's negative and zero sequence currents [1]. Passive compensation is reported to have used reactive components to eliminate those of zero-sequence current at unbalanced voltages. However,

it is only suitable for static loads [6], [7]. The static synchronous compensator (STATCOM) initially functions as voltage control and reactive compensation in power networks. Moreover, it mitigates unbalanced load [8], making it an active compensator. The current decomposition for this unbalanced load compensator uses the modified synchronous reference frame (SRF) [9], [10] and instantaneous reactive power theory (IRPT) modification methods [11], [12] with the switch control using space vector pulse width modulation (SVPWM) [13], [14].

The decomposition of unbalanced load currents is directly unable to use the SRF method because it depends on the suitable frequency for fundamental components. An additional SRF calculation using ωt is required based on the negative sequence component analysis. This method is known as the decouple synchronous reference frame (DSRF) [15].

Improving electrical energy quality by reducing reactive power was carried out using power factor correction (PFC) [16], [17]. However, the decrease in power quality due to harmonics on the grid could be improved by using a power filter. Based on static and dynamic load conditions, the reduction is also conducted by using passive power filters (PPF) and active power filters (APF) [18], [19]. The APF, shunt hybrid active power filter (SHAPF), and current decomposition reduce harmonics, reactive power, and load imbalance. Also, the use of APF/SHAPF in load imbalance conditions is carried out, using hysteresis control [20], IRPT [21], [22], SRF [23], [24], and fuzzy [25], [26]. The harmonic currents in this study were reduced through SHAPF with model predictive control (MPC) at a balanced load in three-phased three and four wires [27] and discussed in a separate article. The contribution of this research is the implementation of finite control set model predictive control (FCS-MPC) on an inductor capacitor passive filter (LC)-connected hybrid power filter with varying non-linear loads and its ability to overcome load imbalances.

2. RESEARCH METHOD

2.1. Shunt hybrid active power filter

The proposed SHAPF consists of passive LC and active filters in a three-phase four-wired grid-connected inverter (GCI) Figure 1. The primary function of this SHAPF was to produce harmonic compensation currents when different phases were generated from the switching process, according to the load i_L . It is then observed to have produced a clean waveform from harmonics formed in (1).

$$i_s = i_L + i_f \quad (1)$$

In three-phase four-wire, neutral currents were produced when the load condition was balanced, as indicated in (2).

$$i_n = i_a + i_b + i_c \quad (2)$$

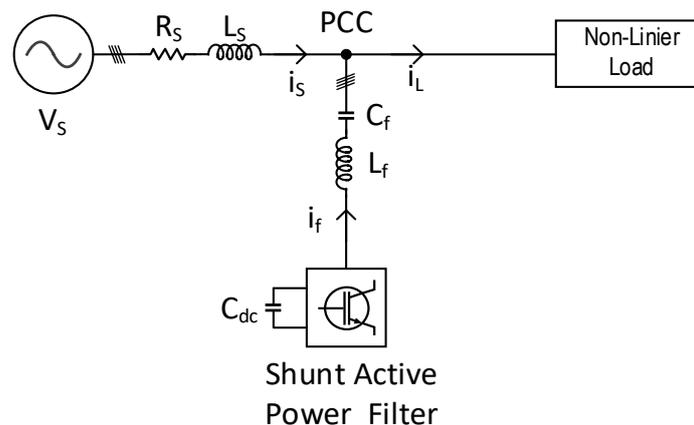


Figure 1. SHAPF circuit with LC

Based on (2), these forms of current flowing in the neutral wire (i_a , i_b , and i_c) were observed to have flowed through phases a, b and c. When the connected load condition was unbalanced, this value did not

equal zero. Therefore, this imbalance condition triggered a difference in the source voltage between the phases and the load current [28]. Furthermore, an adjustment was made to switching the power transistors on the active power filter, originating from the load’s sampling process described in the reference current generation section. This adjustment was found to have been conducted to balance the load.

The installation of the LC filter at the neutral point is used in anticipating the presence of sequence currents that occur due to load imbalances. Besides that, the filter can smooth the ripples of the current wave. The selection of LC parameters in this circuit refers to tareen [29] and tang [30]; by using a ten times approach to the L_s system impedance as well as C, an approximation process is carried out based on reactive power requirements after which the experiment process is carried out.

2.2. Reference current generation

In this process, the decomposition of the load current is carried out to obtain the reference current. The reference current will be used as one of the control components to turn on the transistor as a switch on the hybrid filter. The process of generating this reference current used the SRF method, shown in Figure 2. This SRF method required input from the i_{La} , i_{Lb} , and i_{Lc} loads, in order to produce quadrature and direct axis currents (i_q and i_d) via the use of park transformation [31] as indicated in (3).

$$\begin{bmatrix} i_{Ld} \\ i_{Lq} \\ i_{L0} \end{bmatrix} = \frac{2}{3} \begin{bmatrix} \sin(\omega t) & \sin(\omega t - \frac{2\pi}{3}) & \sin(\omega t + \frac{2\pi}{3}) \\ \cos(\omega t) & \cos(\omega t - \frac{2\pi}{3}) & \cos(\omega t + \frac{2\pi}{3}) \\ \frac{1}{2} & \frac{1}{2} & \frac{1}{2} \end{bmatrix} \begin{bmatrix} i_{La} \\ i_{Lb} \\ i_{Lc} \end{bmatrix} \tag{3}$$

The sine and cos signals ω were obtained from the phase locked loop (PLL) process, with the source voltage as input [32]. This transformation process produced i_d , i_q , and i_0 . The i_d was observed to consist $i_{d(dc)}$ and $i_{d(ac)}$, which were the fundamental and harmonic components of the load in the dq reference frames, as indicated in (4).

$$\begin{cases} i_d = i_{d(dc)} + i_{d(ac)} \\ i_q = i_{q(dc)} + i_{q(ac)} \end{cases} \tag{4}$$

Furthermore, the separation of harmonic currents on the d axis was carried out by pairing the low pass filter (LPF) to pass the i_d current in the fundamental system $i_{d(dc)}$. $i_{d(ref)}$ is obtained from subtracting $i_{d(ac)}$ with $i_{d(dc)}$, which is the result of the LPF process, and the output of this reduction is corrected by i_{dc} . For i_q , the conversion was carried out by reversing the phase, as indicated in (5). The reference signals $i_{d(ref)}$, $i_{q(ref)}$, and $i_{0(ref)}$ were converted into switching currents I_{La}^* , I_{Lb}^* , and I_{Lc}^* , via the use of (6).

$$i_{q(ref)} = -i_q \tag{5}$$

$$\begin{bmatrix} I_{La}^* \\ I_{Lb}^* \\ I_{Lc}^* \end{bmatrix} = \begin{bmatrix} \sin(\omega t) & \cos(\omega t) & 1 \\ \sin(\omega t - \frac{2\pi}{3}) & \cos(\omega t - \frac{2\pi}{3}) & 1 \\ \sin(\omega t + \frac{2\pi}{3}) & \cos(\omega t + \frac{2\pi}{3}) & 1 \end{bmatrix} \begin{bmatrix} i_{d(ref)} \\ i_{q(ref)} \\ i_{0(ref)} \end{bmatrix} \tag{6}$$

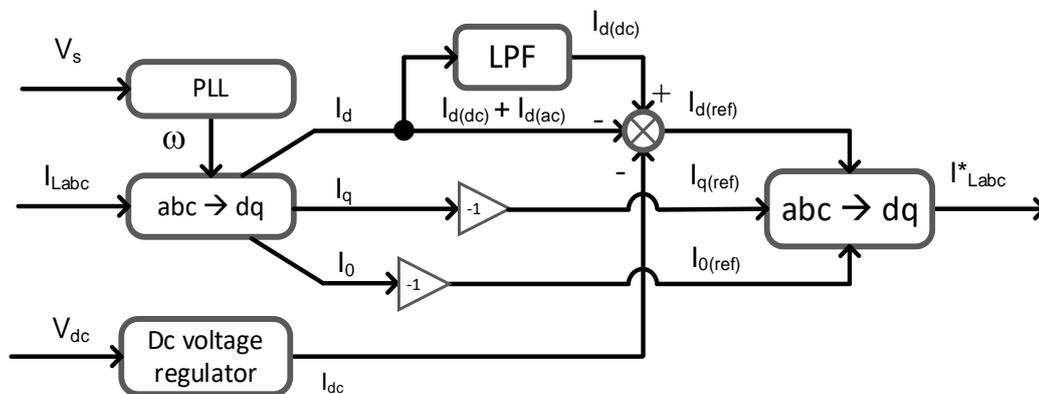


Figure 2. dq reference current generation

2.3. Model predictive control

In this SHAPF, the function of MPC was to control the switching of the four-leg inverter. The MPC started from the input current, $i_{La}^*(k)$, which was then extrapolated through the third-order lagrange method [33] in order to produce $i_{ref}(k+1)$, as indicated in (7). Afterwards, this extrapolated output stream was inputted into the cost function.

$$i_{ref}(k+1) = 4i_{Labc}^*(k) - 6i_{Labc}^*(k-1) + 4i_{Labc}^*(k-2) - i_{Labc}^*(k-3) \quad (7)$$

The components $i_{Labc}^*(k)$, $i_{Labc}^*(k-1)$, $i_{Labc}^*(k-2)$ and $i_{Labc}^*(k-3)$, where the number of reference currents at time $t=k, k-1, k-2$, and $k-3$, respectively, where the sample period was 50 μ s.

The prediction model was then built in (8), a closed-loop circuit.

$$v_{pccx} = L_f \frac{di}{dt} + R_f i + \frac{1}{C_f} \int idt + v_{inv} \quad (8)$$

v_{pccx} was the point of common coupling (PCC) voltage in phase x. However, v_{inv} was observed as the inverter voltage, S_a, S_b, S_c , and S_d , with v_{dc} (input voltage). Also, L_f, R_f , and C_f represented the inductance, resistance, and capacitance of the PPF. Moreover, (8) was then discretized for the derivative, using (9) and (10) for integration via the forward Euler.

$$\frac{di}{dt} \approx \frac{i(k+1)-i(k)}{T_s} \quad (9)$$

$$\frac{1}{C_f} \int idt = \frac{T_s}{C_f} (i(k+1) + i(k))I \quad (10)$$

Afterwards, (8) was rearranged by (9) and (10) with inputs, which were in the form of v_{dc} (DC voltage), $i_f(k)$ (filter current), and the output voltage of the inverter v_{inv} , in order to produce $i_{pre}(k+1)$, as indicated in (11).

$$i(k+1) = \frac{1}{\left(\frac{L_f}{T_s} + R_f + \frac{T_s}{C_f}\right)} \left(v_{pccx} - v_{inv} - \left(\frac{T_s}{C_f} - \frac{L_f}{T_s}\right) (i(k)) \right) \quad (11)$$

The resulting extrapolated and prediction model currents, $I_{ref}(k+1)$ and $I_{pre}(k+1)$, were then compared in order to make observations based on the 16 switching conditions of $I_{pre}(k+1)$, which approached $I_{ref}(k+1)$. Therefore, the slightest difference between $i(k+1)$ and $I_{pre}(k+1)$ with that particular switching condition was used in the system.

2.4. Unbalanced load

When an unbalanced non-linear load was connected to a four-wire three-phase distribution grid, the system became imbalanced and produced symmetrical components from positive, negative, and zero sequences. Moreover, decomposition analysis was carried out via the Fortescue transformation, as indicated in (12) [34]:

$$\begin{bmatrix} i_a \\ i_b \\ i_c \end{bmatrix} = \begin{bmatrix} 1 & 1 & 1 \\ 1 & a & a^2 \\ 1 & a^2 & a \end{bmatrix} \begin{bmatrix} i_0 \\ i_1 \\ i_2 \end{bmatrix} \quad (12)$$

where $a=1\angle 120^\circ$, i_0, i_1, i_2 is the zero, positive, and negative symmetric sequence components. Afterwards, (12) was inputted into (3) to determine the number of negative sequence currents. Therefore, these negative sequence currents were eliminated in order for the load conditions to be balanced. In order to determine the hugeness of the current imbalance, which was measured by (14), Patel's method was carried out [35].

$$K_{ir} = \frac{i_2}{i_1} \quad (13)$$

$$\%I_{unb} = \frac{|I_{a,rms} - I_{b,rms}| + |I_{b,rms} - I_{c,rms}| + |I_{c,rms} - I_{a,rms}|}{I_{a,rms} + I_{b,rms} + I_{c,rms}} \times 100 \quad (14)$$

3. RESULTS AND DISCUSSION

Simulations were carried out on a three-phase four-wired electricity distribution grid, using a PCC voltage of $V_s=400$ volts, with a fundamental frequency, inductance, and resistance of $f_0=50$ Hz, $L_s=5$ mH, and $R_s=0.001 \Omega$, respectively. Moreover, the grid-connected and unbalanced non-linear load. Simulation to test modelling using MATLAB/Simulink. The first test was carried out with a single-phase rectifier load connected to each resistor and inductor (RL) load to represent a balanced loading condition. Afterwards, the second test was non-linear with unbalanced loads in one phase. Furthermore, each phase's third simulation was carried out with different loads to represent three-phase imbalanced loading. This simulation model is presented in Figure 3.

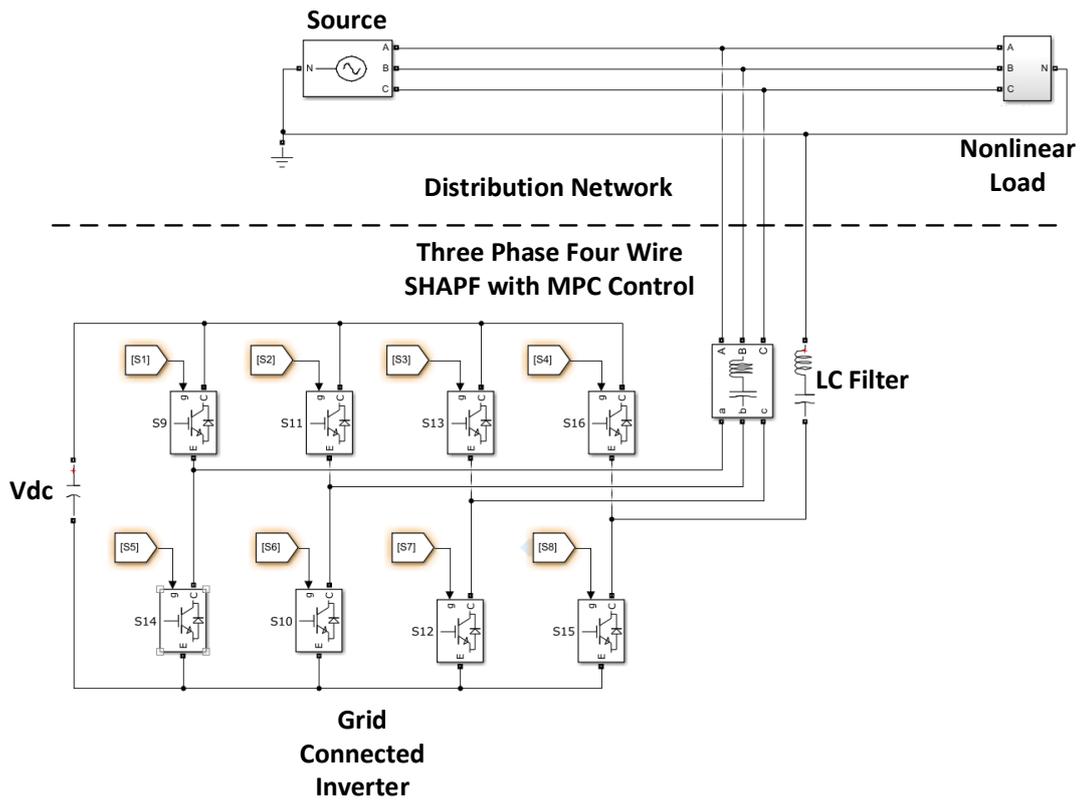


Figure 3. Simulation model

3.1. Simulation on balanced load

The simulation is performed using a balanced load. This balanced load takes the form of a single-phase rectifier circuit connected to a resistor and an inductor, as shown in Figure 4. The value of this load parameter is shown in Table 1.

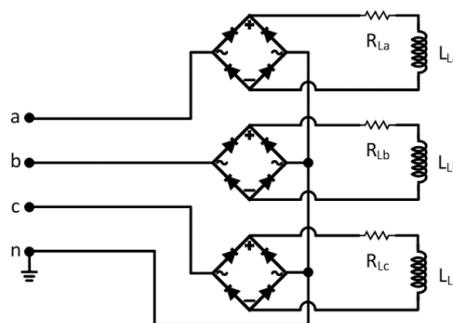


Figure 4. Load circuit

The results of the load voltage experienced distortion, as indicated in Figure 5(a), with the source current resulting in total harmonics distortion of source current (THDi) and average root mean square (RMS) values of 22.16% and 11.6 A, which were presented in 5(b). The SHAPF installation on the balanced load succeeded in reducing the harmonic current so that the THDi became 4.2%. The current waveform can be seen in Figure 5(c).

Table 1. Load parameter

Parameter	Value
$R_{L_a}=R_{L_b}=R_{L_c}$	25 Ω
$L_{L_a}=L_{L_b}=L_{L_c}$	40 mH

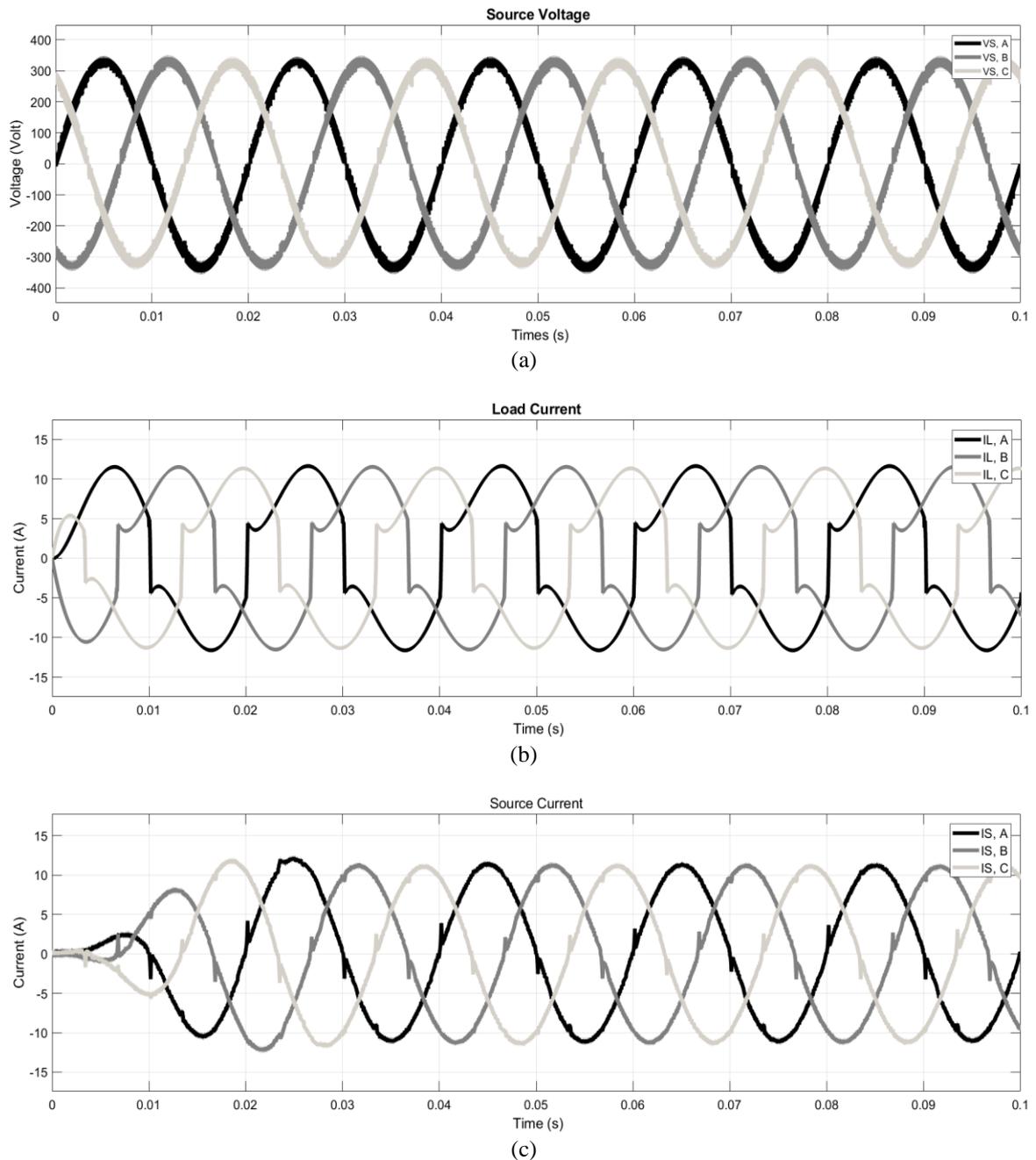


Figure 5. Balanced load test result waveform (a) PCC voltage, (b) load current without SHAPF, and (c) load current with SHAPF

The balanced non-linear load simulation results showed a significant reduction in THD and neutral current, with small ripples. The reference current generated by each phase also had the same shape. The current wave spectrum can be seen in Figure 6(a) there is a significant decrease, especially low-order harmonics, to below 2%, while the voltage wave spectrum can be seen in Figure 6(b) changes do not occur significantly due to the FCS-MPC process in the cost function does not involve the voltage equation as part of the GCI switching pattern. Figure 7(a) shows the current harmonic wave spectrum before SHAP installation, where the current waveform is not purely sinusoidal, while in Figure 7(b), there is a change in the current waveform due to a decrease in THD.

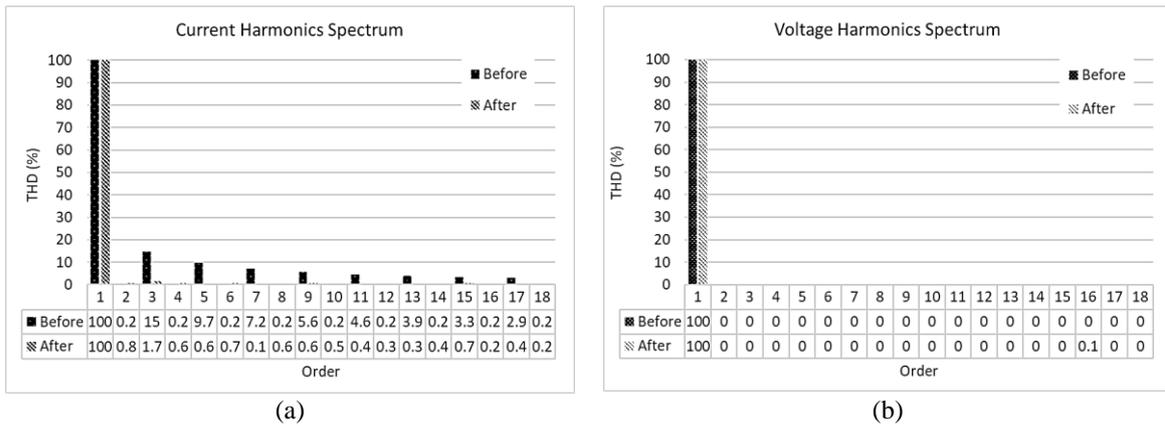


Figure 6. Harmonic spectrum (a) current spectrum and (b) voltage spectrum

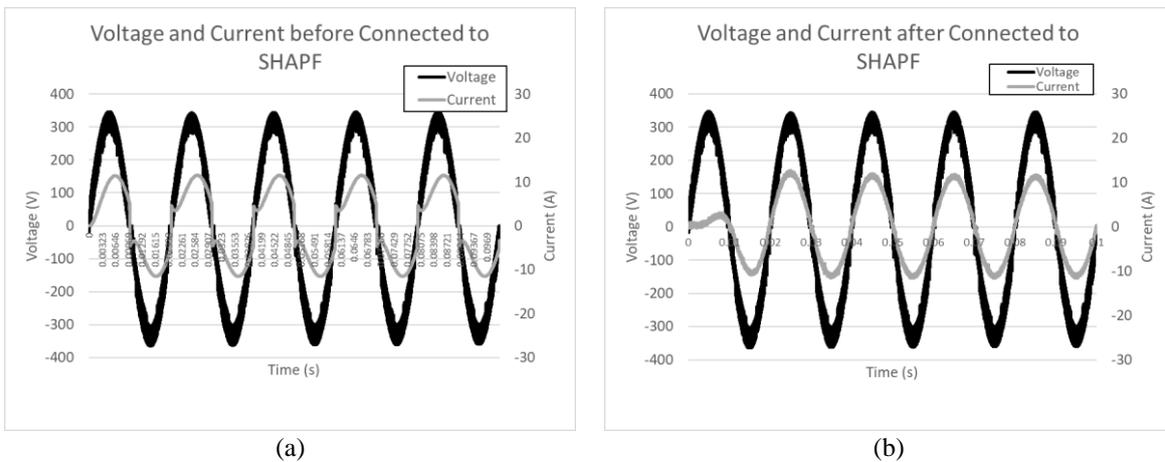


Figure 7. Voltage and load current waveform (a) before installation of SHAPF and (b) after installation of SHAPF

3.2. Simulation on unbalanced load

Based on the unbalanced load simulation, the circuit is shown in Figure 4, with the test being carried out by changing the load parameter, as indicated in Table 2. The waveform of the simulation results for the load current is presented in Figure 8(a). As a result of this load imbalance, a different measured RMS current value was produced using (14). However, the THD of the different load phases also experienced a difference in value, with $THDi_a$, $THDi_b$, and $THDi_c$ at 21.95%, 39.72%, and 21.80%, respectively. By having the same RMS current for phases, a and c at 11.63 amperes, the value of b is 20.33 amperes; from Patel (14), we get the current imbalance $\%I_{unb}=39.9\%$. The installation of SHAPF on the PCC side resulted in a decrease in $THDi$ that occurred in $THDi_a$, $THDi_b$, and $THDi_c$ by 3.31%, 5.65%, and 3.36%, respectively. The installation of SHAPF succeeded in reducing the $\%I_{unb}$ load imbalance through (14), from 39.96% to 0.94%. The current waveform of the harmonic reduction is shown in Figure 8(b). The current harmonic spectrum can be seen in Figure 9(a), while the voltage harmonic spectrum can be seen in Figure 9(b).

Table 2. Load parameter

Parameter	Value
$R_{La}=R_{Lc}$	25 Ω
R_{Lb}	13 Ω
$L_{La}=L_{Lc}$	40 mH
L_{Lb}	60 mH

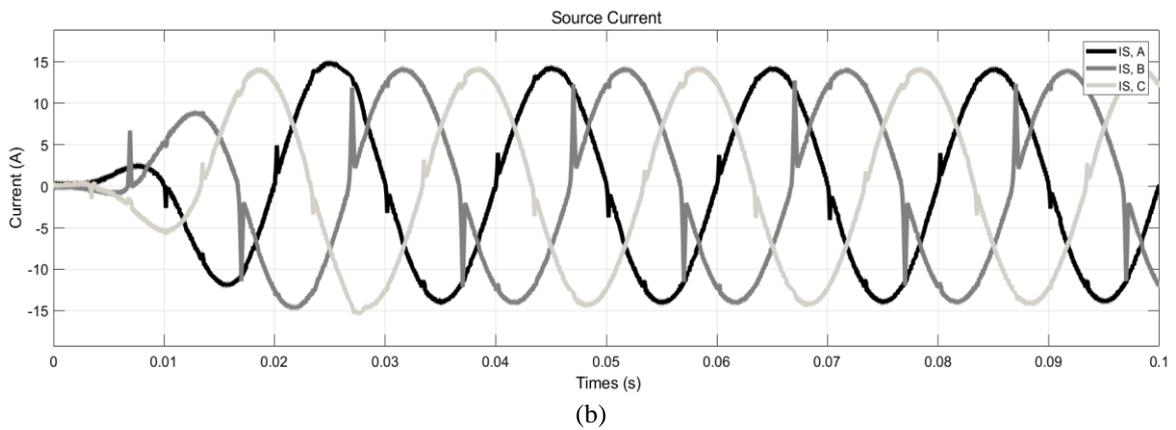
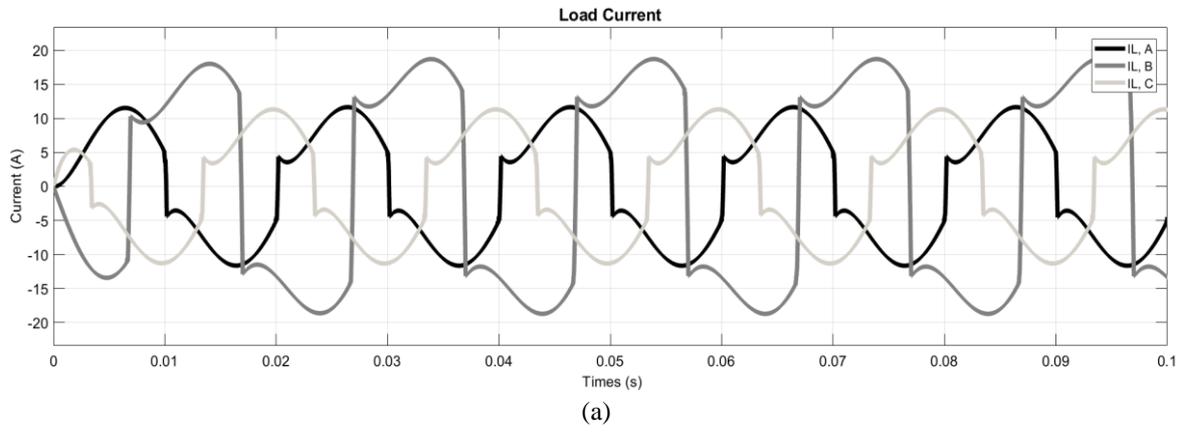


Figure 8. Unbalanced load simulation result waveform (a) current waveform without SHAPF and (b) current waveform with SHAPF

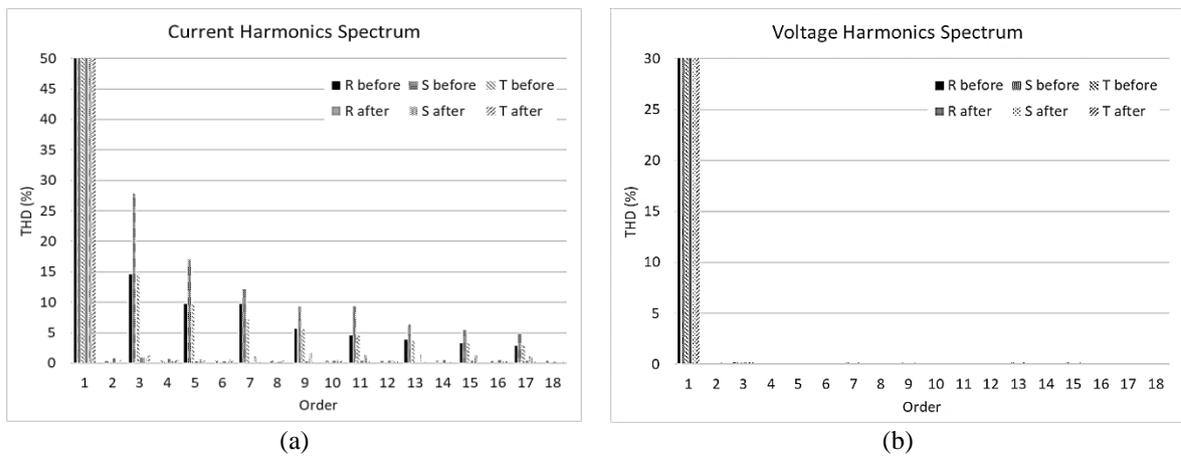


Figure 9. Harmonics spectrum under unbalanced load (a) current harmonics spectrum and (b) voltage harmonics spectrum

The load imbalance was also observed to have caused a neutral current flow. The simulation also showed a decrease in the RMS neutral current from 6.137 to 0.2 A. The neutral current waveform before and after the installation of SHAPF was presented in Figures 10(a) and 10(b).

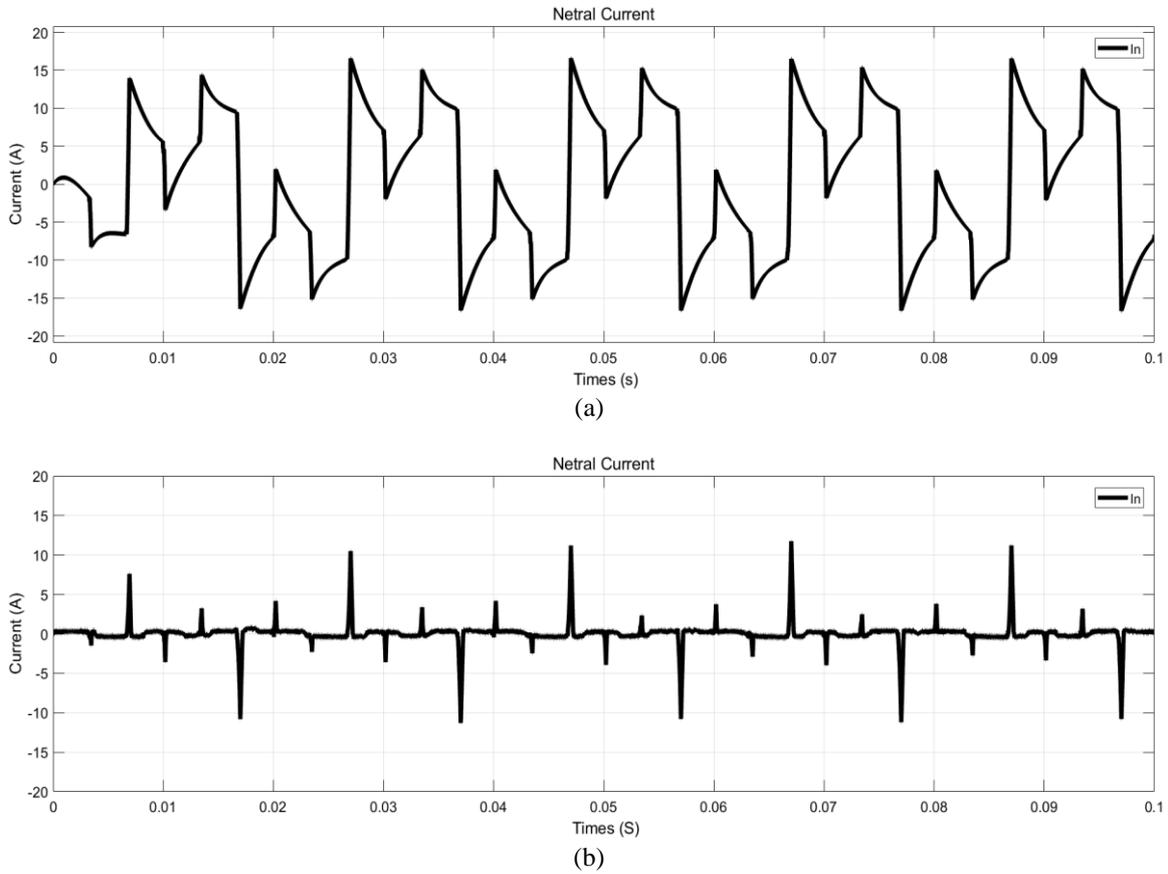


Figure 10. Neutral current at load on unbalanced load (a) without SHAPF and (b) with SHAPF

3.3. Simulation with different loads for each phase

This test is carried out with the topology in Figure 4, with each load having a different value, as shown in Table 3. The difference in each phase’s load produced a loaded waveform with different harmonics and RMS values of i_a , i_b , and i_c , at 11.62, 20.37, and 7.98 A, respectively. The waveform of the imbalance current is presented in Figure 11(a). A calculation based on (14) found that % I_{unb} was 62.01%, with $THDi_a$, $THDi_b$, and $THDi_c$, 21.61%, 39.08%, and 18.62%, respectively.

The harmonic reduction process that occurs at an unbalanced load is presented in Figure 11(b). The THD reduction process is still running normally, even though the simulated load does not match the SHAPF rating. There was also a decrease in the current imbalance % I_{unb} by 0.42% and THD to an average of 4.74%. This load imbalance produces a neutral current of 11.29 A. After the hybrid power filter is installed, the neutral current becomes 0.3 A. The current waveforms before and after the installation of SHAPF are represented in Figures 12(a) and 12(b), while the current harmonic spectrum can be seen in Figure 13(a), and the voltage harmonic spectrum can be seen in Figure 13(b).

Table 3. Load parameter

Parameter	Value
R_{La}	25 Ω
R_{Lb}	13 Ω
R_{Lc}	37 Ω
L_{La}	40 mH
L_{Lb}	60 mH
L_{Lc}	50 mH

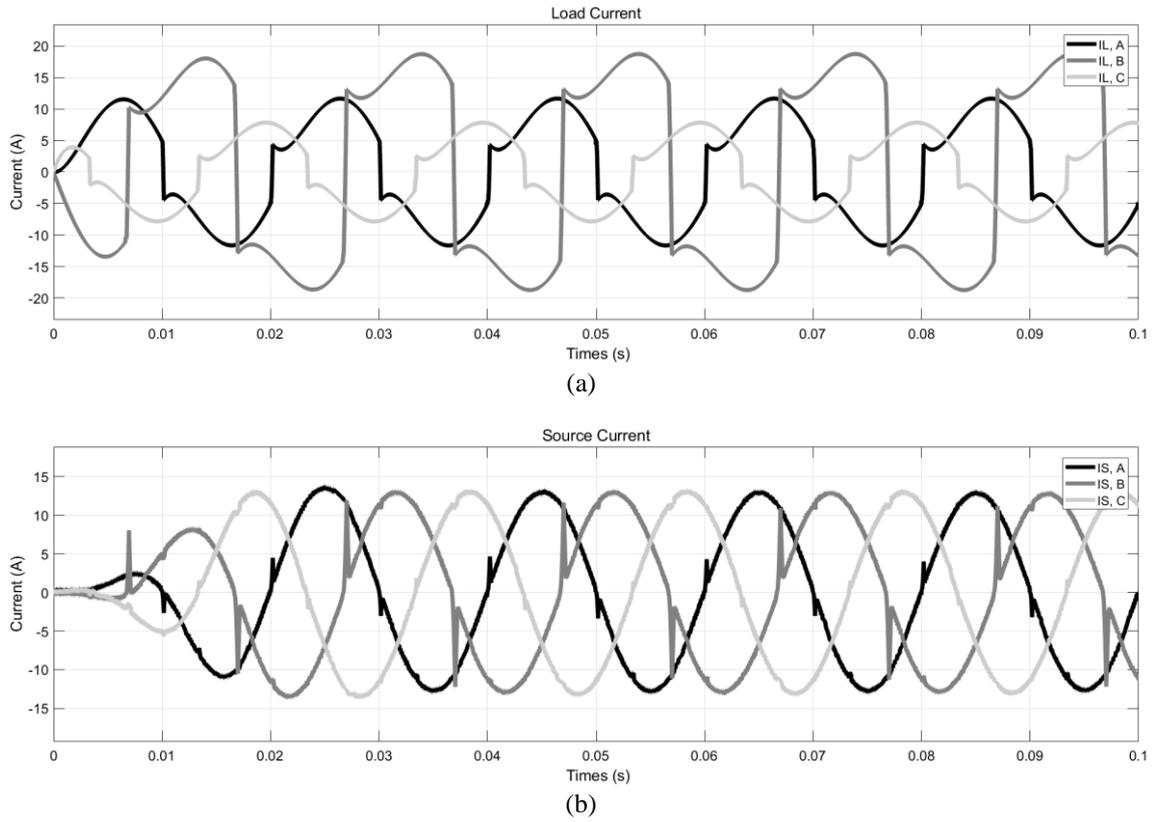


Figure 11. The current waveform at different loads for each phase (a) without SHAPF and (b) with SHAPF

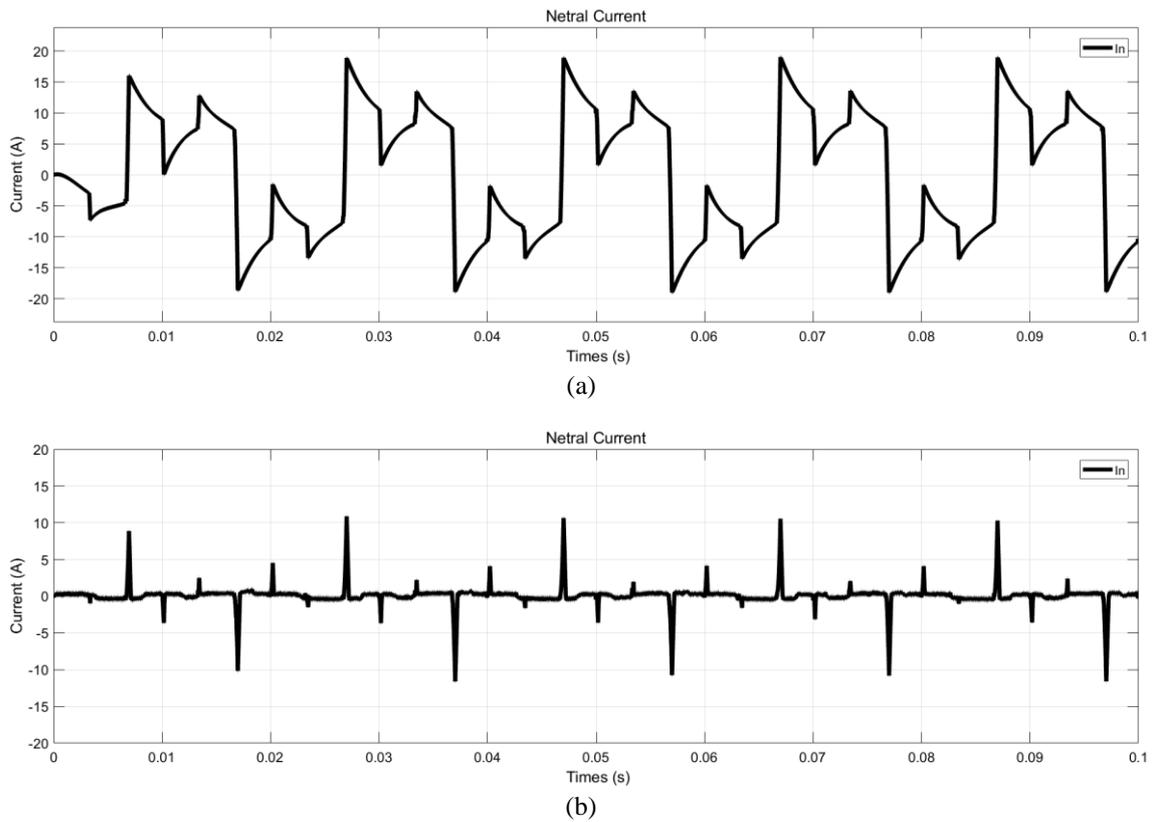
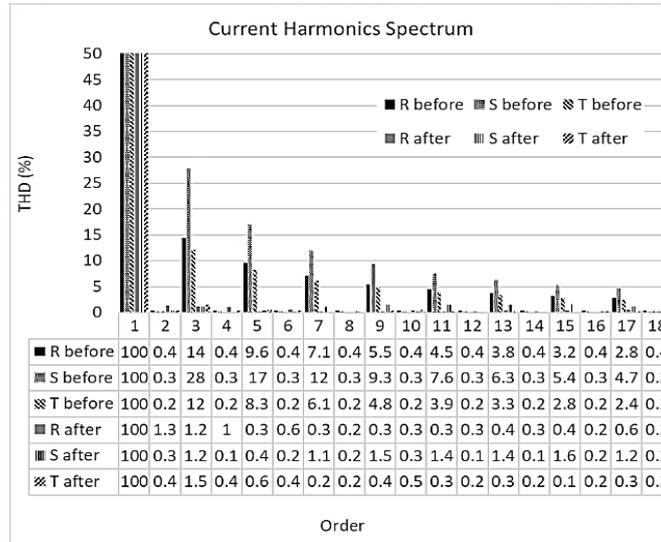
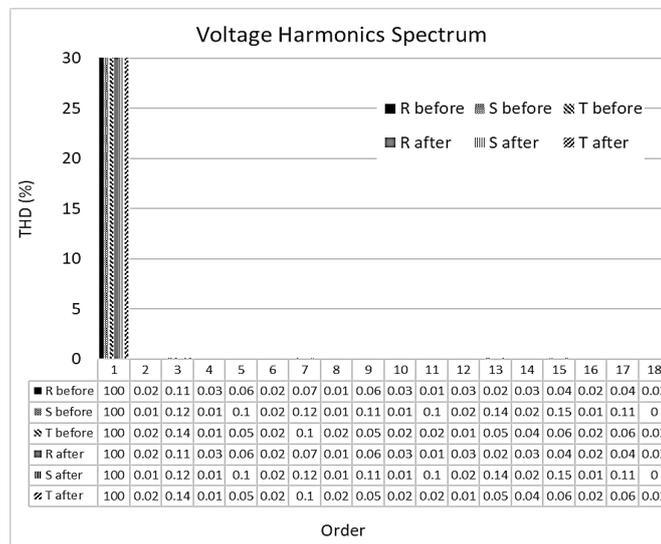


Figure 12. Neutral current on different loads for each phase (a) without SHAPF and (b) with SHAPF



(a)



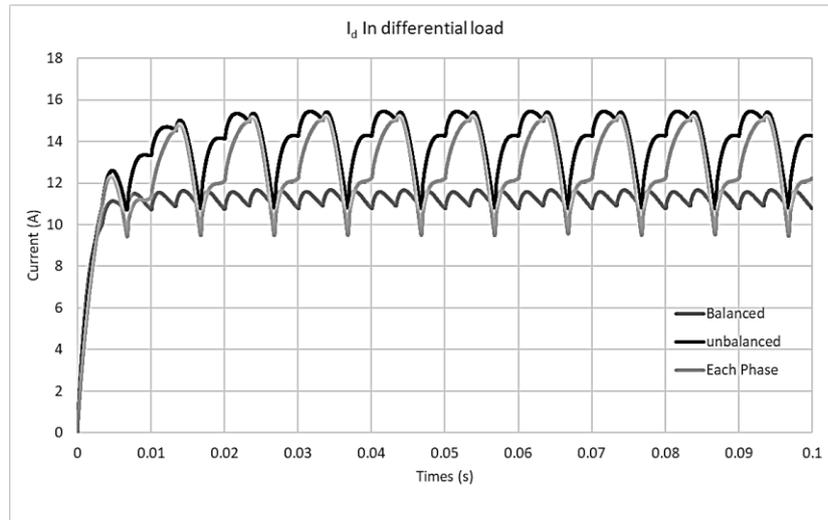
(b)

Figure 13. Harmonics spectrum under different load (a) current harmonics spectrum and (b) voltage harmonics spectrum

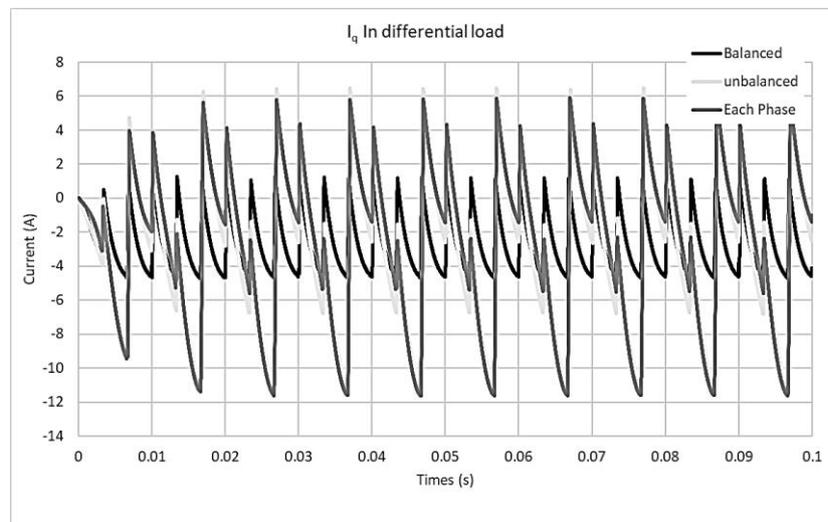
3.4. Discussion

The extraction of load currents to determine harmonics was carried out by the dq method to produce i_d and i_q components. In balanced loads, the i_d and i_q currents do not contain negative or zero symmetric sequence components. However, when the load was unbalanced, either one of the phases was not balanced, with i_d and i_q currents containing sequence components in negative and zero orders. The effect of the load imbalance on i_d and i_q currents was further illustrated in Figure 14.

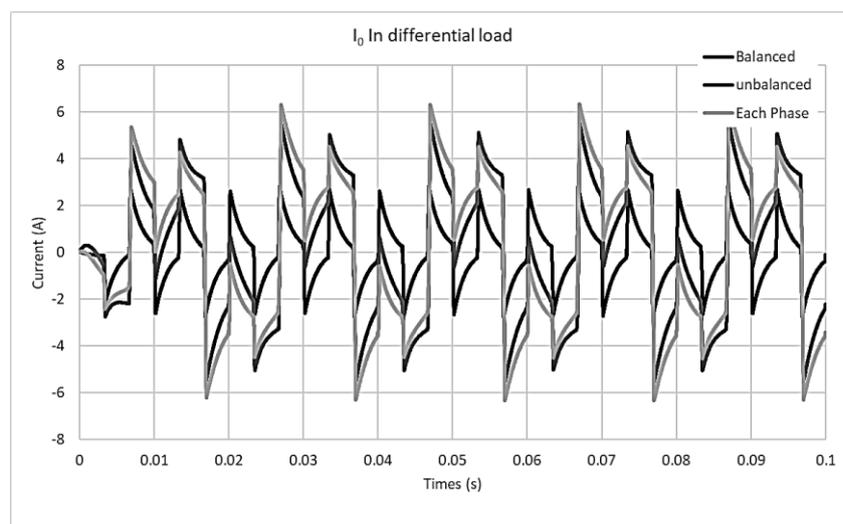
Figure 14(a) shows that the current i_d produces a small ripple wave with no peak variation for a balanced load. However, one of the phases results in a more complicated curve for unbalanced loads. In addition, the waveform indicates load imbalance. Figure 14(b) shows that the current ripple for i_q at a balanced load is less than that at an unbalanced load, resulting in very different magnitudes between the two conditions. According to the neutral currents in Figure 14(c), even though the inrush charge is small for a balanced load, they still produce an i_0 signal. However, the magnitude of the observed current is enormous for an unbalanced load. In decomposing the symmetrical component for unbalanced loads, two SRF blocks were needed to anticipate the negative sequence’s angular value and the opposite ωt . The resulting product was a better reference current, as it still reduced imbalances in this simulated study.



(a)



(b)



(c)

Figure 14. Extraction current in i_d , i_q , and i_0 (a) i_d with different load conditions, (b) i_q in different load conditions, and (c) i_0 in different load conditions

4. CONCLUSION

This paper highlights how to mitigate harmonics in distribution networks at unbalanced loads. Harmonic mitigation is done by injecting the current generated from the converter switching process. This injection current is obtained by comparing the reference current and the predicted current in the cost function to produce a power switch ignition pattern. Unbalanced current decomposition using the SRF method could balance the load current and reduce harmonics. In the simulation results on MATLAB/Simulink, the harmonics at the balanced load can be reduced to comply with the IEEE 519-2014 standard. At unbalanced non-linear loads, where current flows in the neutral wire, the installation of a hybrid power filter can reduce the harmonics and, at the same time, reduce the current in the neutral wire so that the imbalance can be reduced. Therefore, this filter can be used at varying and unbalanced loads in each phase, especially in industrial use.

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