A trade-off design of microstrip broadband power amplifier for UHF applications

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ABSTRACT

In this paper, the design of a Broadband Power Amplifier for UHF applications is presented. The proposed BPA is based on ATF13876 Agilent active device. The biasing and matching networks both are implemented by using microstrip transmission lines. The input and output matching circuits are designed by combining two broadband matching techniques: a binomial multi-section quarter wave impedance transformer and an approximate transformation of previously designed lumped elements. The proposed BPA shows excellent performances in terms of impedance matching, power gain and unconditionally stability over the operating bandwidth ranging from 1.2 GHz to 3.3 GHz. At 2.2 GHz, the large signal simulation shows a saturated output power of 18.875 dBm with an output 1-dB compression point of 6.5 dBm of input level and a maximum PAE of 36.26%.

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1. INTRODUCTION

Nowadays, the world as we know it would be unimaginable without Microwave and Radio Frequency electronics. At home, on our phones, in our cares or everywhere, we receive signals from satellites (Satellites TV receivers) or from Global Positioning Systems (GPS), as well as from base stations. The RFID (Radio Frequency Identification) devices becoming more and more deployed in our life and finding use in the non-line-of-sight applications. Indeed, almost every communication system has some sort of transceiver, and intrinsically a power amplifier (PA) running in one of the following bands defined by IEEE standard 521-1984: L – S – C – X or Ku band [1-4].

In fact, PA applications finding use in a broad range of fields including detection and navigation applications (radars, GPS…), telecommunication, medical microwave imaging, avionics, microwave induction heating and many other applications. As a result, the PA considerations may extremely differ in design, architecture and technological requirements [5-13]. However, the rapid evolution of the wireless communications driven particularly by the growing demand to transmit an increasing amount of data, and the subsequent system level specifications, besides the growing need of broad bandwidth has resulted in an increased regard in Broadband Power Amplifiers (BPAs). Such components can replace multiple narrowband PAs with a single device, and consequently reduce the hardware research and develop costs owing to the incompatibility of the new and old wireless communication standards. [14-19]

In order to improve the broadband performance, various architectures and configurations of BPAs have been developed including balanced configurations, distributed structures and traveling wave approach besides several matching techniques mostly based on optimization algorithms such as Real Frequency Technique (RFT) and the subsequent Simplified RFT (SRFT), filter-type design and the load-pull technique [20-22]. For the traveling wave or distributed PA approach, the broad bandwidth, linearity and flat gain are achieved by applying a linear design method. However, the drawback of this technique resides in the high number of components deployed to reach the same performance as a single PA, and this results in large size, low efficiency levels and high cost. On the other hand, the balanced structure has good VSWR and gain flatness over about a two octave bandwidth, but the main weakness of such configuration lies in the higher noise figure and the lower Power Added Efficiency (PAE) due to the additional loss in the coupler. For the RFT, SRFT and filter type design, the main drawback of those techniques is resulted in the use of matching networks based on lumped elements (inductors, capacitors and resistors) which must afterwards be transformed into distributed structures. However, in a broad frequency band, this transformation is very tedious and presents various difficulties [23-26].

In this work, a novel and simple BPA configuration is introduced, which cover the mainstream wireless communication standards running in Ultra High Frequency (UHF) band from 1.2 GHz to 3.2 GHz. The proposed BPA is implemented on FR4 substrate and based on GaAs ATF13876 active device. In order to achieve the broadband performance various matching techniques are used in parallel. The first technique is based on multi-section binomial transformer while the second techniques is an approximate transformation of previously synthesized lumped element matching networks into transmission line matching networks. The proposed circuit compares favorably with the contemporary state-of-the-art.

This paper is organized as follows: in section 2, the proposed Broadband Power Amplifier design is described including, topology, broadband biasing circuit and matching techniques applied in this work. Section 3 is devoted to the simulation results, while the concluding statements are summarized in section 4.

2.   THE PROPOSED BROADBAND POWER AMPLIFIER DESIGN METHOD

Whether characterized as narrowband, wideband, high efficiency, low noise, high power or otherwise, the aim of an RF power amplifier is to provide a finite positive power gain over the operating frequency band. In other words, the main task of an RF PA is to amplify the power level of the signal at its input up to a predefined level at its output over the operating bandwidth. From a practical standpoint, the PA design is usually a trade-off, attempting to realize various conflicting requirements including gain flatness vs broad bandwidth, low distortion vs high output power or efficiency vs linearity. Referring to Figure 1, a typical single stage RF power amplifier consists of a single RF active device, connected to the source and the load through an input and output matching circuits respectively, and DC supplied through a DC bias network. Both the source and the load having the same system characteristic impedance Z_0.
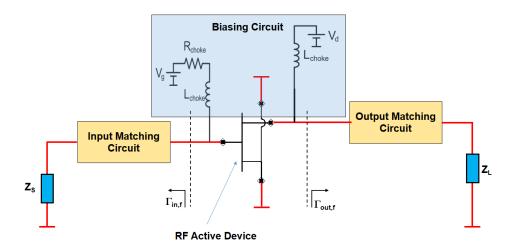

Figure 1. Single active device power amplifier block diagram

2.1. Broadband matching networks synthesis

In high frequencies, due to the natural mismatching of the active device with the source and load impedances, the use of impedance matching networks is indispensable, if not, a part of the electrical signal propagated through the PA will be reflected. In other words, to provide a maximum transfer of the RF power from the input port to the output port through the RF active device, an input matching network must be placed between the latter and the input port, besides an output matching network between the active device and the output port.

On the other hand, the main driven in the BPA design is the need for broad bandwidth. Consequently, the synthesized impedance matching networks must cover a bandwidth as broad as possible. Basically, the impedance matching circuits are designed according to the targeted application as well as the desired operating frequency, and this resulted in various matching techniques and schemes, mostly based on approximation equations, that can be used in order to reach the broadband performance.

At high frequencies, in spite of the lumped element matching networks are intuitive and allows various options for design, excepting an MMIC, it is usually very difficult to implement on a PCB substrate. On the contrary, the matching circuits based on transmission lines, in particular on microstrip technology, are easy to implement on PCB substrate. For this reason, the first broadband matching technique used in this work is an approximate transformation of lumped element matching networks into microstrip matching networks. This transformation can be realized by swapping the lumped elements with transmission lines Figure 2 illustrates the microstrip transmission lines that can replace the synthesized lumped element matching network.
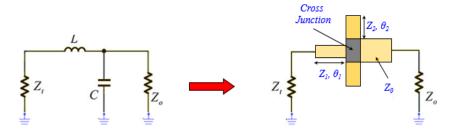

Figure 2. Transformation of lumped element matching circuit into microstrip matching circuit

The microstrip line having characteristic impedance Z_1 and length θ_1 can be considered as a series inductor L, while the two parallel-connected open microstrip stubs with characteristic impedance Z_2 and length θ_2 can be regarded as shunt capacitor C. Z_1, Z_2, θ_1 and θ_2 values can be approximately calculated by the help of (1) and (2):

$$X_1 = Z_1 \tan(\theta_1) \approx \omega L \qquad \text{With} \qquad \theta_1 = \beta l_1 \qquad (1)$$

$$X_2 = \frac{Z_2}{2\tan(\theta_2)} \approx \omega C \qquad \text{and} \qquad \theta_2 = \beta l_2 \qquad (2)$$

Since the variables in (1) are Z1 and θ_1, there are two degrees of freedom in implementing the inductor value. The higher characteristic impedance Z1 is, the closer to an inductor it will be. Similarly, the two degrees of freedom are also valid for (2) and the lower characteristic impedance Z2 is, the closer it can be implemented as a shunt capacitor.

It is worth noting that the field of the cross-junction depicted in Figure 2 may be influenced when an external circuit is directly connected to the matching network, especially, a coaxial connector. However, to avoid this, a transmission line having the same characteristic impedance Z0 is added. The second broadband matching technique used in this work is the multi-section quarter wave transformer. The schematic of the latter is shown in Figure 3, in which the characteristic impedance of the load ZL is transformed to the feed line impedance Z0. The transformer is composed of discrete transmission lines sections having dissimilar characteristic impedances, but their electrical lengths βl are identical, which are assumed to be a quarter wave length at the center frequency f0 of the operating bandwidth. Since the transformer is symmetrical, the return loss coefficients at the junctions between sections are associated as:

$$|\Gamma_0| = |\Gamma_N| \; ; \; |\Gamma_1| = |\Gamma_{N-1}| \; ; \ldots$$

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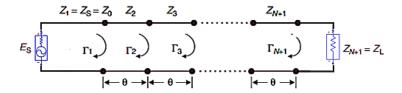
Figure 3. Multi-section quarter wave impedance transformer

Practically, two multi-section quarter wave impedance transformers are widely known, namely Chebyshev and Binomial []. In this work, we adopt the Binomial transformer, which is based on approximate theory. The fractional bandwidth of the binomial transformer illustrated in Figure 3 is given by:

$$\frac{\Delta f}{f_0} = \frac{2(f_0 - f_m)}{f_0} = 2 - \frac{\pi}{4} cos^{-1} \left| \frac{2\rho_m}{\ln(Z_L/Z_0)} \right|^{1/N} \tag{3}$$

Where ρ_m is the tolerable reflection coefficient in the passband and N is the number of sections. The section impedances can be approximately calculated by using (4), defined as:

$$ln\frac{Z_{n+1}}{Z_n} = 2\rho_n = 2^{-N} C_n^N ln\frac{Z_L}{Z_0} \qquad Where: \qquad C_n^N = \frac{N!}{(N-n)!n!} \tag{4}$$

Where ρn is the reflection coefficient at the junction between Zn and Zn+1, C_n^N are the binomial coefficients, and Zn and Zn+1 are the impedances of the nth and (n+1) th sections respectively.

2.2. Broadband microstrip biasing circuitry design

At RF frequencies, the biasing circuitry design take an important part in the design of the power amplifier. In fact, it provides for the active device the sufficient bias conditions in terms of voltage and current, and therefore guarantee a maximum performance of the whole circuit. Basically, there are various biasing configuration that can be used for this purpose. Simplified microwave biasing circuits as shown in Figure 4.
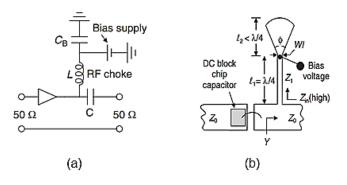

Figure 4. Simplified microwave biasing circuits: (a) lumped element biasing. (b) microstrip biasing

Typically, a microwave biasing network consists of an RF choke and DC block as shown in Figure 4 (a). However, at RF frequencies, the RF choke are usually implemented by using a high impedance transmission line as shown in Figure 4 (b). The aim of RF choke is to prevent the RF signal from leaking into the biasing network. As a result, the RF chock is designed in a such a way that present a very high impedance in the operating frequency band. Similarly, the DC block is assumed that present a short circuit at the operational bandwidth.

In this work, the biasing circuit is an integral part of the output matching network. Moreover, we adopt the microstrip biasing technique because, as mentioned above, it is easy to implement on PCB substrate besides it provides a broad bandwidth and the capacitor value must be selected such that the $wC = 2\pi fC$ is very high.

2.3. Stability considerations

Fundamentally, any power amplifier must meet the stability conditions in the operating frequency band, otherwise, the unavoidable parasitic effects are enough to provide oscillations and therefore turned the power amplifier into an oscillator. Two stability factor are widely used for the purpose to fulfill the unconditionally stability namely Rollet factor k and Bodway factor B1, defined by the following:

$$B_1 = 1 + |S_{11}|^2 + |S_{22}|^2 - |\Delta|^2 > 0 \tag{5}$$

$$k = \frac{1 + |\Delta|^2 - |S_{11}|^2 - |S_{22}|^2}{2|S_{12}S_{21}|} > 1 \tag{6}$$

Where

$$\Delta = S_{11}S_{22} - S_{12}S_{21} \tag{7}$$

From (5) and (6), the unconditionally stability is accomplished only when the following conditions are fulfilled simultaneously: $k > 1$, $S_{22} < 1$ and $B_1>0$. By using the ideas reported above, the proposed Broadband Power Amplifier schematic circuitry is illustrated in Figure 5.
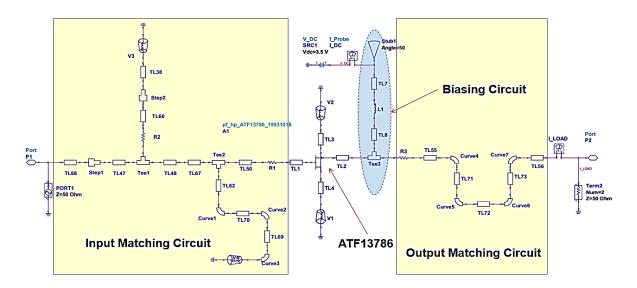

Figure 5. The proposed single stage microstrip BPA schematic circuitry

3. RESULTS AND DISCUSSIONS
3.1. Small signal simulation and stability analysis

At microwave frequencies, it is very difficult to perform a two port network by using the immittance parameters because there are based on short/open circuit terminations. As a result, the characterization of the proposed BPA is based on the Scattering parameters which are defined from reflected and incident waves. In this work, the source and load terminations both having the same impedance Z=50 Ω. The scattering parameters of the proposed BPA are simulated by using ADS software.

The simulated Small-Signal S-parameters are illustrated in Figure 6. As it can be noted, both the input and output reflection coefficients (S_{11} & S_{22}) are strictly under – 11dB, while the maximum value reached by the revers transmission coefficient (S12) is – 20 dB over the frequency band ranges from 1.2 GHz to 3.2 GHz. The small signal power gain (S_{21}) also changes between a maximum value of 16.17 dB and a minimum value of 8 dB over the operating frequency band. From a practical standpoint, the revers transmission coefficient (S_{12}) represents the internal feedback of a two port network, and the smaller value of S12, the greater is the degree of stability and isolation of a given two-port network.

The curves of stability factors are shown in Figure 7. From (5) and (6), we can clearly assume that the unconditionally stability of the proposed BPA is fulfilled over the wider operating bandwidth. The small signal simulation shows satisfying results in terms of broadband impedance matching, power gain and stability over the wider bandwidth ranging from 1.2 GHz to 3.2 GHz.
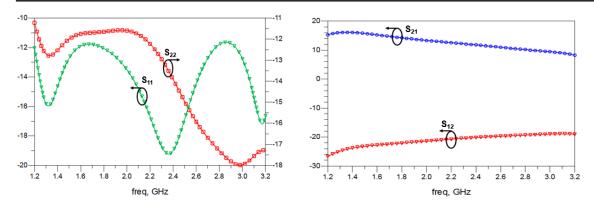
Figure 6. Input return loss [S_{11}], output return loss [S_{22}], power gain [S_{21}] and revers transmission coefficient [S_{12}] versus frequency
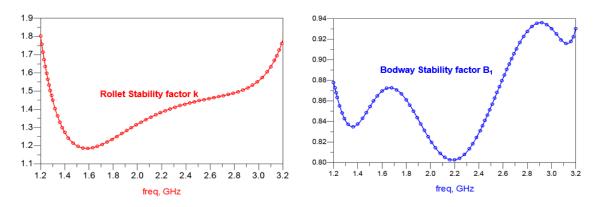

Figure 7. Rollet and bodway stability factors versus frequency

3.2. Large signal performance

The large signal performance of the proposed BPA has been simulated at 2.2 GHz. Basically, the output power is defined as the power delivered toward the external load in a given frequency band. However, while the input power increase, we eventually reach a point where the output power can anymore keep increasing linearly with the input power. This point is point is named the 1-dB compression point, in which, the output power deviates from its linear region by 1-dB. Figure 8 (a) shows the simulated output power and the 1-dB compression point of the of the proposed BPA. This latter reaches a saturated output power of 18.875 dBm, this corresponding of 77.17 mW, with an output 1-dB compression point of 6.5 dBm of input level.
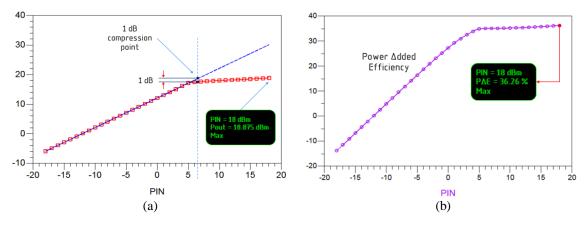

Figure 8. Output power (a) and power added efficiency (b) versus input power

Moreover, from a practical point of view, in a given frequency band, a power amplifier may be considered as a device able to transform the DC power from supplies into RF power. The effectiveness of this transformation is usually evaluated by means of Power Added Efficiency (PAE). The simulated PAE is shown in Figure 8 (b). The proposed BPA achieves a maximum PAE of 36.26%.

In order to provide a general overview of the deployed matching techniques capabilities and the proposed BPA performance, a comparison with the similar contemporary state-of-the art BPAs is performed. Table 1 summarizes a performance comparison between the proposed BPA and the recently reported BPAs. To author's best acknowledge, the proposed BPA provides competitive results in terms of broadband matching, power gain, output power and PAE.

Table 1. Performance comparison between the proposed BPA and state – of –the art BPAs

PAs	Freq [GHz]	Gain [dB]	Psat [dBm]	PAE [%]	S11 [dB]	S22 [dB]	Supply [V]
[27] 2015	1.9 – 2.7	11	28.1	13.7	-	-	2.5
[28] 2016	1.8 – 2.8	28	25	6.1	-12 | -25	-9 | -19	5
[5] 2017	1 – 4	12.15	14.8	20	-9 | -24	-8 | -18	3.5
[20] 2018	1.1 - 3	14.9	17.14	14.9	-10 | -35	-10 | -25	3
[14] 2019	1 – 12.5	10	-	-	-5 | -15	-5 | -15	V_d=2 V_g=-0.15
This Work	1.2 – 3.2	16.31	18.875	36.26	-11 | -19	-12 | -18	3.5

4. CONCLUSION

A Broadband Power Amplifier operating in the frequency band ranges from 1.2 GHz to 3.2 GHz has been described in this paper. The proposed BPA is based on ATF13876 active device and cover the mainstream applications running in UHF band. The deployed matching and biasing techniques shows an excellent input and output matching as well as an unconditionally stability over the overall operating bandwidth. The large signal simulation exhibits an output 1-dB compression point of 6.5 dBm of input level with a saturated output power of 18.875 dBm (77.17 mW) and a maximum PAE of 36.26%. The proposed BPA provides competitive results compared with the contemporary state-of-the-art BPAs.

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