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A shortest data window algorithm for detecting the power factor in presence of non-sinusoidal load current

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Article Info	ABSTRACT
	During recent years, nonlinear power electronic equipments introduce harmonic pollution on electric power systems. It makes the traditional power factor meter can not act accurately when it monitors unbalanced and harmonic loads. In this paper, a new algorithm for detecting the power factor in presence of non-sinusoidal load current is proposed. The proposed algorithm detects the true power factor exactly. By uses only two successive sampled data points of the voltage and the current for each displacement power factor value calculation and two sampled data points for each distortion power factor value calculation, the total/true power factor becomes easy to measure using these values directly. The proposed detector implemented using microcontroller as a main part and has been tested for single phase power system. The test results show that it can measure the true power factor of the loads quickly and accurately.

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1. INTRODUCTION

Many schems were proposed to measure the power factor. Many of these schemes took a cycle of an input power line frequency for the measurement [1-9], while attempts in [10-13] took only a half a cycle to record the power factor. An attempt to reduce the elapsed time for measurement to about a quarter a cycle was illustrated in [14]. The big advance in speeding up the measurement was achieved in [15], which amounted to only 3 progressive samples of the voltage and current signals. A further great improvement was introduced in [16] where only two progressive samples of the voltage and current which were sampled simultaneously were adequate for the measurement.

All of the mentioned methods were suited for measuring the power factor for pure sinusoidal inputs of the voltage and the current. In practice the load current happens to be non-sinusoidal due to harmonic distortion. A monitoring scheme to measure the instantaneous power factor for a non-sinusoidal signal phase system was described using wavelet transform in [17] then discrete wavelet transformation window in [18].

A FPGA-based chip was implemented to improve the computation errors were described in [19], while a finite element method was reported in [20]. The last four methods involved complex mathematical procedures that were hard to be implemented. This might cause a delay which was not pointed out. Microcontrollers are single-chip computers which offer cost-efficient solutions. Using microcontrollers in the measurement of a power factor was demonstrated in [21, 22].

This paper is meant to make a modification in the paper explained in [16] to promot the method to measure the power factor in the presence of a non-sinusoidal load current. The displacement power factor can be measured by using only two progressive samples of the voltage and current separated by a random short time. Two samples are taken from the output of the voltage controlled oscillator (VCO) of the phase-locked

loop (PLL) circuit and the true r.m.s-to-dc converter to calculate the true power factor as will be described later. The transient time response of the proposed method is very fast and the measured value represents the instantaneous magnitude of the power factor.

2.   THEORY OF CALCULATION
2.1. Displacement power factor calculation

Assuming that the source voltage stays undistorted (sinusoidal, without harmonics), then relating to Figure 1 the two voltage samples are.
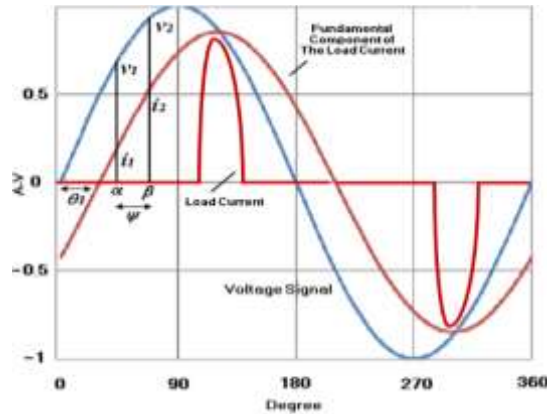

Figure 1. Voltage and current samples

$$v_1 = V_m \sin\alpha \tag{1}$$

$$v_2 = V_m \sin\beta = V_m \sin(\alpha + \psi) \tag{2}$$

$$v_2 = V_m(\sin\alpha \cos\psi + \cos\alpha \sin\psi) \tag{3}$$

$$\frac{v_2}{v_1} = \frac{V_m(\sin\alpha \cos\psi + \cos\alpha \sin\psi)}{V_m \sin\alpha}$$

$$\text{Or} \quad \cot\alpha = \frac{\dfrac{v_2}{v_1} - \cos\psi}{\sin\psi} \tag{4}$$

The two current samples i_1 and i_2 can be written as follows:

$$i_1 = G\, I_1 \sin(\alpha + \theta_1) \tag{5}$$

$$i_2 = G\, I_1 \sin((\alpha + \theta_1) + \psi)$$

$$i_2 = G\, I_1 \left[\sin(\alpha + \theta_1)\cos\psi + \cos(\alpha + \theta_1)\sin\psi \right] \tag{6}$$

where G is the gain of the multiple feedback band pass filter, I_1 is the maximum value of the fundamental component of the load current and θ_1 is the phase angle between the source voltage and the fundamental component of the load current.

$$\text{Now,} \quad \frac{i_2}{i_1} = \cos\psi + \cot(\alpha + \theta_1)\sin\psi$$

$$\text{Hence, } \cot(\alpha + \theta_1) = \frac{\dfrac{i_2}{i_1} - \cos\psi}{\sin\psi} \tag{7}$$

Let $\alpha + \theta_1 = \gamma$

Hence, $\theta_1 = \gamma - \alpha$

$$\text{Now, } \cot\theta_1 = \cot(\gamma - \alpha) = \frac{1 + \cot\alpha \cot\gamma}{\cot\alpha - \cot\gamma}$$

$$\cot\theta_1 = \frac{1 + \left(\dfrac{\dfrac{v_2}{v_1} - \cos\psi}{\sin\psi}\right)\left(\dfrac{\dfrac{i_2}{i_1} - \cos\psi}{\sin\psi}\right)}{\dfrac{\dfrac{v_2}{v_1} - \cos\psi}{\sin\psi} - \dfrac{\dfrac{i_2}{i_1} - \cos\psi}{\sin\psi}}$$

$$\cot\theta_1 = \frac{1 + \dfrac{v_2 i_2}{v_1 i_1} - \left(\dfrac{v_2}{v_1} + \dfrac{i_2}{i_1}\right)\cos\psi}{\left(\dfrac{v_2}{v_1} - \dfrac{i_2}{i_1}\right)\sin\psi}$$

Let $\quad K_1 = \cot\theta_1$

$$\text{So, } K_1 = \frac{\cos\theta_1}{\sqrt{1 - \cos^2\theta_1}} \tag{8}$$

Squaring the last equation and solving for $\cos\theta_1$ (displacement power factor) [23], we get,

$$\text{pf}_{disp} = \cos\theta_1 = \frac{K_1}{\sqrt{1 - K_1{}^2}} \tag{9}$$

where

$$K_1 = \frac{1 + \dfrac{v_2 i_2}{v_1 i_1} - \left(\dfrac{v_2}{v_1} + \dfrac{i_2}{i_1}\right)\cos\psi}{\left(\dfrac{v_2}{v_1} - \dfrac{i_2}{i_1}\right)\sin\psi} \tag{10}$$

The displacement power factor [24] can be calculated depending only on samples (v_1, v_2) and their corresponding current samples (i_1, i_2). The value of (k_1) will be undefined if (v_1 or i_1) are taken at zero crossing and in this case,

a) When $V_1 = 0$

$v_1 = V_m \sin\alpha$, hence, $\alpha = 0$ or π

$$i_1 = GI_1 \sin(\alpha + \theta_1) = GI_1 \sin\theta_1 \tag{11}$$

$$i_2 = GI_1 \sin\theta_1 \cos\psi + GI_1 \cos\theta_1 \sin\psi \tag{12}$$

From (11) & (12)

$$\frac{i_2}{i_1} = \cos\psi + \cot\theta_1 \sin\psi$$

$$\cot\theta_1 = \frac{\dfrac{i_2}{i_1} - \cos\psi}{\sin\psi}$$

Assume, $\quad K_2 = \dfrac{\dfrac{i_2}{i_1} - \cos\psi}{\sin\psi}$ \hfill (13)

Using the same procedure used previously in obtaining (9), we get that,

$$pf_{disp} = \cos\theta_1 = \frac{K_2}{\sqrt{1 - K_2^2}} \hfill (14)$$

It is obvious that in this case the *p.f.* can be calculated depending on the current samples only.

b) When $i_1 = 0$

$$i_1 = GI_1 \sin(\alpha + \theta_1) = 0$$

Hence, $\theta_1 = -\alpha$, or $\theta_1 = \pi - \alpha$, and

$$\cot\theta_1 = \frac{\cos\psi - \dfrac{V_2}{V_1}}{\sin\psi}$$

Assuming, $K_3 = \dfrac{\cos\psi - \dfrac{V_2}{V_1}}{\sin\psi}$ \hfill (15)

Using the same procedure used previously in obtaining equation (9) & (14), we get that,

$$pf_{disp} = \cos\theta_1 = \frac{K_3}{\sqrt{1 - K_3^2}} \hfill (16)$$

and the p.f. can be calculated in this case depending only on the voltage samples. $\sin\psi$ and $\cos\psi$ can be calculated from (5) and (6) as follows

$$\sin(\alpha + \theta_1) = \frac{i_1}{G\,I_1} \hfill (17)$$

$$\cos(\alpha + \theta_1) = \left[1 - \left(\frac{i_1}{G\,I_1}\right)^2\right]^{\frac{1}{2}}$$

$$\sin(\alpha + \theta_1 + \psi) = \frac{i_2}{G\,I_1}$$

$$\cos(\alpha + \theta_1 + \psi) = \left[1 - \left(\frac{i_2}{G\,I_1}\right)^2\right]^{\frac{1}{2}}$$

$$\sin\psi = \sin((\alpha + \theta_1 + \psi) - (\alpha + \theta_1))$$

$$\sin\psi = \sin(\alpha + \theta_1 + \psi)\cos(\alpha + \theta_1) - \cos(\alpha + \theta_1 + \psi)\sin(\alpha + \theta_1)$$

$$sin\,\psi = \left(\frac{i_2}{G\,I_1}\right)\left[1 - \left(\frac{i_1}{G\,I_1}\right)^2\right]^{\frac{1}{2}} - \left(\frac{i_1}{G\,I_1}\right)\left[1 - \left(\frac{i_2}{G\,I_1}\right)^2\right]^{\frac{1}{2}} \tag{18}$$

$$\cos\psi = cos((\alpha + \theta_1 + \psi) - (\alpha + \theta_1))$$

$$\cos\psi = cos(\alpha + \theta_1 + \psi)\cos(\alpha + \theta_1) + \sin(\alpha + \theta_1 + \psi)\sin(\alpha + \theta_1)$$

$$\cos\psi = \left[1 - \left(\frac{i_2}{G\,I_1}\right)^2\right]^{\frac{1}{2}}\left[1 - \left(\frac{i_1}{G\,I_1}\right)^2\right]^{\frac{1}{2}} - \left(\frac{i_2}{G\,I_1}\right)\left(\frac{i_1}{G\,I_1}\right) \tag{19}$$

The output of VCO has [25] a peak value equal to the maximum expected value of GI_1. Hence,

$$i_1 = G\,I_1\sin(\alpha + \theta_1)$$

$$V_{os}(t) = GI_1\exp\,\sin(\alpha + \theta_1)$$

where $V_{os}(t)$ is the instantaneous value of the output from the voltage controlled oscillator while GI_{1exp} is the maximum expected value of GI_1.

Hence, the actual value GI_1 can be calculated instantaneously by dividing any one of the two input current samples (i.e. i_1 or i_2) by the corresponding sample from the output of the voltage control oscillator (i.e. V_{os1} or V_{os2}) and multiplying the result by the GI_{1exp}.

$$GI_1 = \frac{i_1}{V_{os1}} \times GI_1\exp \tag{20}$$

Or

$$GI_1 = \frac{i_2}{V_{os2}} \times GI_1\exp \tag{21}$$

2.2. True power factor calculation

The distortion power factor (Pf_{dist}) describes how the harmonic distortion of a load current decreases the average power transferred to the load [26].

$$pf_{dist} = \frac{1}{\sqrt{1 + THDI^2}} = \frac{I_{1,rms}}{Irms} = \frac{I_1}{\sqrt{2}Irms}$$

THDI is the total harmonic distortion of the load current. This definition assumes that the voltage stays undistorted. $I_{1,rms}$ is the fundamental component of the current and I_{rms} is the total current - both are square values. Knowing the gain of multiple feedback band pass filter (G) and the value of (GI_1) which achieved using (20) or (21) then,

$$pf_{dist} = \frac{1}{G}\left(\frac{GI_1}{\sqrt{2}Irms}\right) \tag{22}$$

The result when multiplied with the displacement power factor (Pf$_{disp}$) is the overall, true power factor or just power factor (Pf$_{true}$):

$$\mathrm{pf}_{true} = pf_{disp} \times pf_{dist} = \cos\theta_1 \times \frac{1}{G}\left(\frac{GI_1}{\sqrt{2}Irms}\right) \qquad (23)$$

3. HARDWARE IMPLEMENTATION
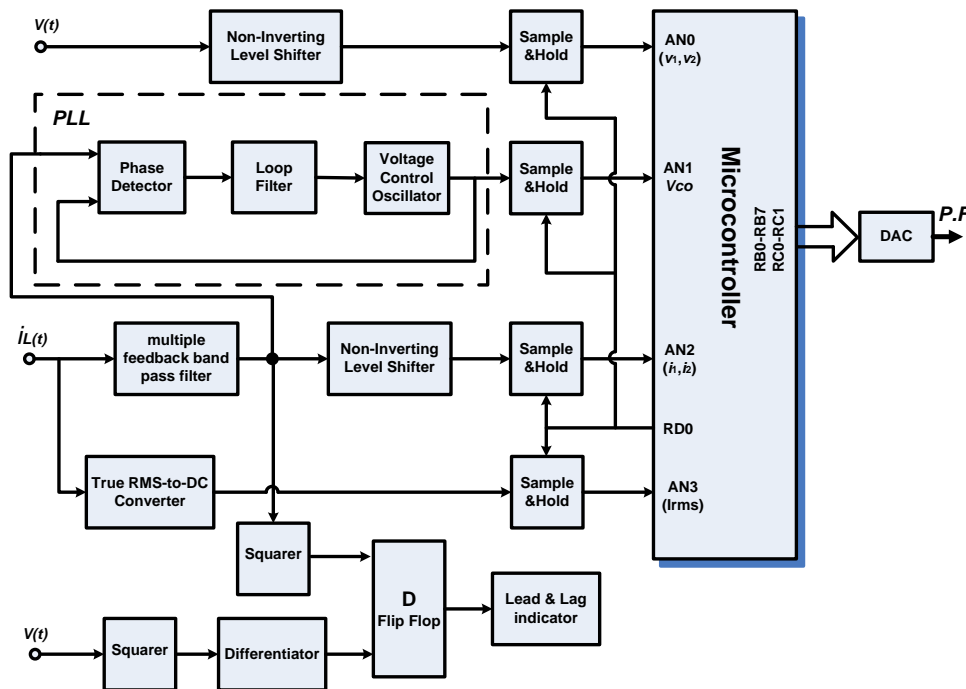The system block diagram is shown in Figure 2.

Figure 2. The system hardware

3.1. General hardware parts description
The PIC18F452 microcontroller is used due to comprising a 10-bit 8-channel Analog-to-Digital converter (A/D) embedded module, 32KB flash code memory, 1,536 Bytes of RAM, its smaller size, and low cost. The output of the PLL voltage controlled oscillator is adjusted so that it's in phase with the output of the multiple feedback band-pass filters which represent fundamental component of the load current signal multiply by *G* gain. The AD536A is used as true rms-to-dc Converter it's directly computes the true rms value of any complex ac (or ac plus dc) input waveform of the load current and gives an equivalent dc output level. A common task is to convert a positive to negative signal into a range suitable for a single supply PIC ADC. The two Non-Inverting Op-Amp Level Shifter circuits shown in Figure 2 will convert a ± 5V signals which represent the voltage and current values into a 0 to 5V signal.

3.2. Microcontroller ADC module acquisition time
A full 10-bit conversion takes 12 A/D cycles to make a complete convertion. This is estimated to be 19.2μs. Adding to this a best acquisition time possible which amounts to 12.1μs. Thus to achieve one complete conversion of 31.3μs is needed. After the convertion is attained additional two convertion periods are required to resume any new convertion process which amounts to 3.2μs. Hence the total conversion time is 34.5μs. The reciprocal of this number gives a maximum sampling frequency of about 29 KHz.
As mentioned before the power factor calculation method needed voltage and current samples acquired at the same time and because of the conversion time of A/D module therefore a high speed sample and hold amplifiers (SHA) with same a control signal is used to prevent any error might be occurs [21] .

3.3. Lead-lag indicator

The lead-lag indicating signal is obtained by using the D flip-flop. The square current signal is used as data input to the D flip-flop, while the square voltage signal is differentiated and fed as a clock input to the flip-flop. The flip-flop output Q will go to the state that is present on the D input whenever a positive transition occurs at the clock input, therefore Q will indicate the lead status and $\overline{Q}$ indicate the lag status as shown in Figure 3.
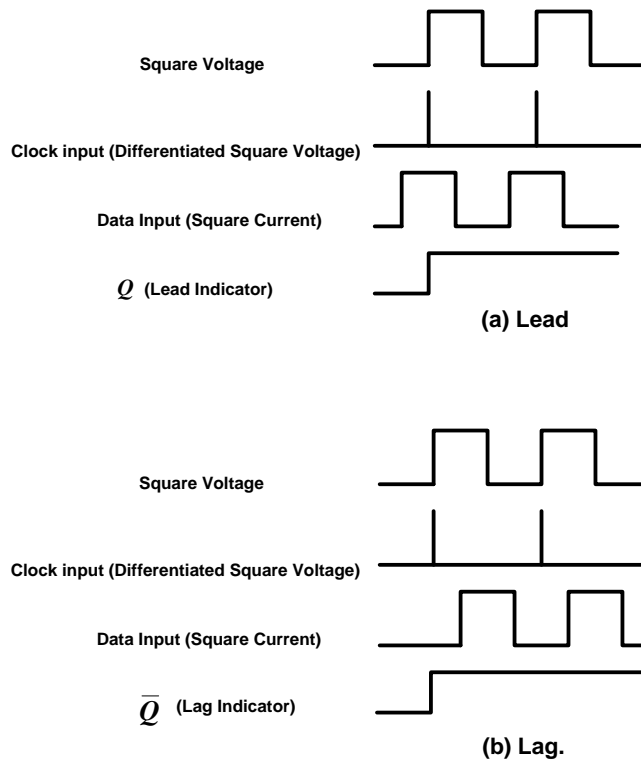
Figure 3. The waveform arrangement of lead-lag indicators

4.   SOFTWEAR IMPLEMENTATION

The flowchart for the firmware of the microcontroller is shown in Figure 4. The firmware program has been written in 'C' language, using the mikroC compiler (mikroElektronika). The true_pf.C contains the functions, which initializes the microcontroller, acquires the two samples from the input voltage signal and the corresponding two current samples in addition to two samples from the VCO and true rms-to-dc converter. The values of each samples are rescaling to be in the range of (-2.5 to 2.5) V. Calculating the values of GI1 using (20), sinψ, cosψ using (18, 19) and calculating the value of K1, K2 or K3 using one of the three cases according to (10, 13, and 15) .The final value of displacement power factor achieved using one of (9, 14, and 16). The output DC voltage from detector which equivalent to the true power factor is achieved by using (22, 23) and forming it as 10 bit word before sending it to R2R DAC through port B and the least two bit of port C.

```
                    ( Start )
                        |
            +---------------------------+
            |         Configure         |
            | - Port B and C as output  |
            |    - Port D as ouput      |
            |  - ADC converter module   |
            +---------------------------+
                        |
    / Send the first control signal to  SHA through RD0 /
                        |
    / Read the first two samples of the analog data (voltage /
    / and current) from ADC channels AN1 & AN2 and /
    / save them to signed variables v1 & i1 respectively /
                        |
    / Read the sample of analog data (voltage control oscillator) /
    / from ADC cannel AN0 and save it to signed variable (Vosc) /
                        |
    / Send the second  control signal to  SHA through RD0 /
                        |
    / Read the second two samples of the analog data /
    / (voltage and current) from ADC channels AN1 & AN2 /
    / and save them to signed variables v2 & i2 respectively /
                        |
    / Read the sample of analog data (True RMS value) from /
    / ADC cannel AN3 and save it to signed variable (Irms) /
                        |
    +-----------------------------------------+
    | Rescaling the values (Vosc, i1, i2, v1, v2) to be in the |
    |          range (-2.5 - 2.5) V           |
    +-----------------------------------------+
```

$$GI_1 = \frac{i_1}{Vos} \times GI_1 exp$$

Clculate the value of $\cos\psi$ & $\sin\psi$

$v_1 = 0$? — Yes

No

$i_1 = 0$? — Yes / No

$V_{ratio} = v_2 / v_1$ | $V_{ratio} = v_2 / v_1$ | $I_{ratio} = i_2 / i_1$

Calculate K3 | $I_{ratio} = i_2 / i_1$ | Calculate K2

Calculate K1

Calculate (*Pfdisp*) using K1 or K2 or K3

$$pf_{dist} = \frac{1}{G} \left(\frac{GI_1}{\sqrt{2} Irms} \right)$$

$$pf_{true} = pf_{disp} \times pf_{dist}$$

Forming pftrue value as 10-bit word

Outputting the least eight bits through port B

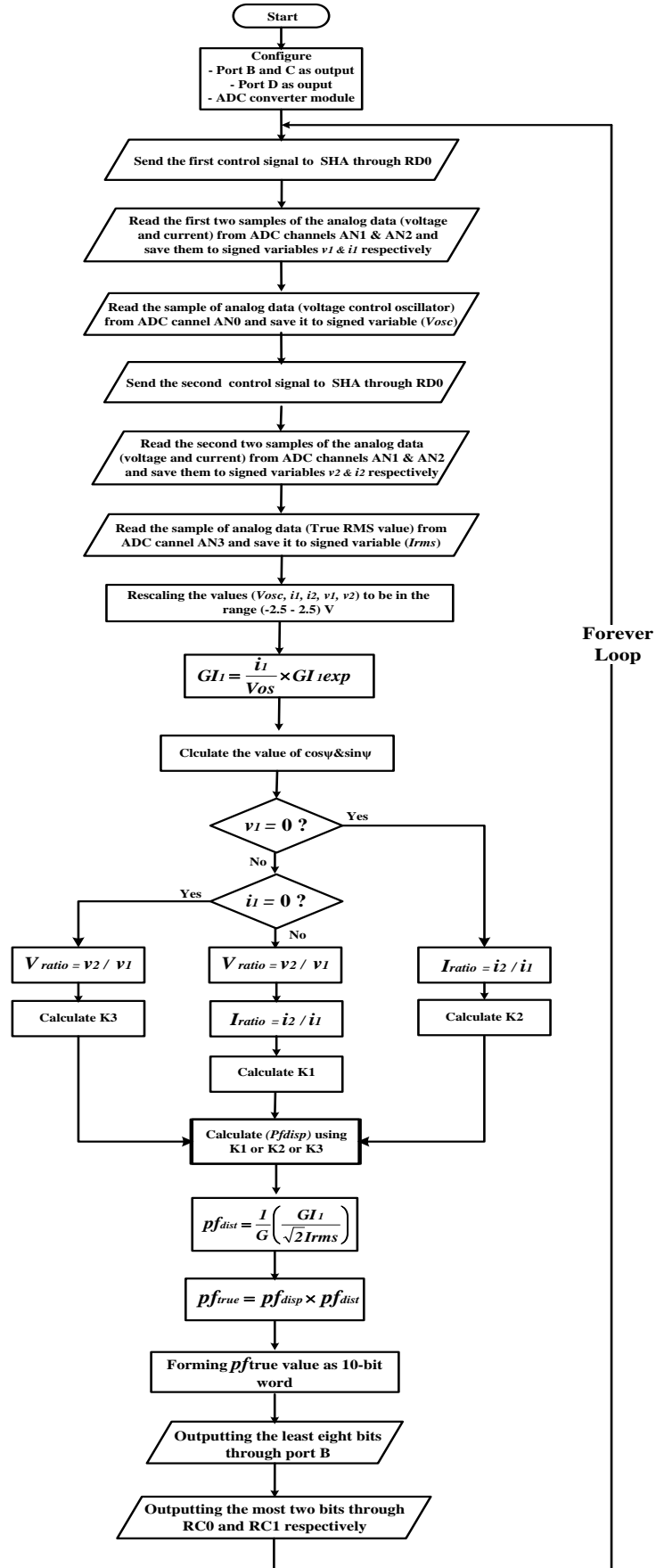Outputting the most two bits through RC0 and RC1 respectively

Forever Loop

Figure 4. System flowchart

5.   EXPERIMENTAL RESULTS

The proposed power factor detector method is developed as per the scheme mentioned above. The experimental tests is for non-sinusoidal load current situations and have been carried out using the proposed detector along with Fluke 43B power quality analyzer in order to evaluate the accuracy of the proposed detector. The measurements results for different seven common single-phase residential loads are given in Table 1, where it is seen that their current distortion levels tend to fall into the following three categories: low (*THDI* ≤ 20%), medium (20% < *THDI* ≤ 50%), high (*THDI* > 50%).

Table 1. Test results for non-sinusoidal load current

| Load | Fluke 43B | | | | | Proposed detector | | | | percent |error|, % |
	pf disp	pf disp nature	THDI	pf dist	pf true	output (Vdc)	pf true	Lead indicator	Lag indicator	
Load 1	0.9990	lagging	1.8	1.000	0.9990	4.9910	0.9982	0v	+5v	0.080
Load 2	0.8750	lagging	13.4	0.991	0.8671	4.3375	0.8675	0v	+5v	0.046
Load 3	0.9980	lagging	18.2	0.984	0.9820	4.9075	0.9815	0v	+5v	0.051
Load 4	0.9514	lagging	26.0	0.968	0.9206	4.6050	0.9210	0v	+5v	0.043
Load 5	0.9559	leading	39.5	0.930	0.8891	4.4470	0.8894	+5v	0v	0.034
Load 6	0.9874	leading	121.0	0.637	0.6294	3.1445	0.6289	+5v	0v	0.079
Load 7	0.9983	leading	140.0	0.581	0.5804	2.9000	0.5800	+5v	0v	0.069

6.   PROPOSED DETECTOR EXPERIMENTAL RESULT CURVE

The theoretical calculated values of power factors and the measured with the help of proposed detector are matching with less than 0.1 % error. Figure 5 shows the detector experimental result and theoretical power factor curves for θ from 0° to 90°. The small deviation in the experimental curve is mainly due to the limitation of ADC module and R2R DAC resolution (10 bit).
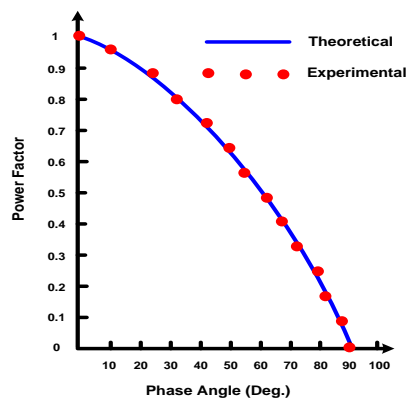

Figure 5. Shows the proposed detector experimental result curve

7.   CONCLUSION

A new short data window algorithm for detecting the power factor of a single-phase system in presence of non-sinusoidal load current is described. A novel algorithm has been developed for displacement power factor calculation, the proposed detector required two progressive samples of the voltage and current separated by random short time to calculate displacement power factor in addition to one sample taken from the outputs of voltage controlled oscillator of the phase-locked loop and the true rms-to-dc converter to accomplish the calculation of the true power factor. The proposed algorithm is implemented based on microcontroller with few support circuits. The maximum acquisition and calculation time limit is determined mainly by the maximum sampling frequency of the ADC and the overall speed of the microcontroller that used. Increasing the ADC and DAC numbers of bits will reduce the percentage errors that appeared in experimental test for the detector. The investigation reveals that the proposed power factor detector can successfully be used for online monitoring of the true power factor. It is also indicates leading or lagging nature of measured power factor. The developed detector is highly reliable and possesses enough flexibility to suit the requirement of electric power system with nonlinear power electronic equipments introduce harmonic distortion on load current.

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Safaa S Omran was born in Iraq. He graduated from University of Baghdad in 1978, and then he got the MSc from the same University in 1984. Now he is working in the Electrical Engineering Technical College / Middle Technical University as an assistant prof. His interest working researches are in the field of microprocessor design for embedded systems, Image processing and cryptography system design.

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