

## Solar energy based impedance-source inverter for grid system

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### Article Info

#### Article history:

Received Apr 30, 2018

Revised Sep 10, 2018

Accepted Sep 30, 2018

#### Keywords:

Impedance-source inverter

Maximum power point tracking (MPPT)

Solar energy system

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### ABSTRACT

In this work, the fickleness of solar energy can be overcome by using Maximum Power Point Tracking algorithm (MPPT). Perturb and Observation (P&O) MPPT algorithm accomplish fast the maximum power point for rapid change of environmental conditions such as irradiance intensity and temperature. The MPPT algorithm applied to solar PV system keep the boost converter output constant. Output from boost converter is taken to three phase impedance-source inverter with RL load and grid system. Impedance-source inverter performs the transformation of variable DC output of the solar PV system in to near sinusoidal AC output. This near sinusoidal AC output consecutively is served to the RL load first and then to grid system. The simulation is carried out in matlab/simulink platform both for RL load and grid system and the simulation results are experimentally validated for RL load arrangement only.

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## 1. INTRODUCTION

With reduce in cost of solar PV modules and the growing electricity tariffs, grid connected solar PV systems are becoming economically feasible for the vast population. The grid connected solar PV systems offers various benefits like greater energy production, high efficiency levels, low current total harmonic distortion, fast maximum power point tracking which deliver high power yields etc. The grid connected solar PV systems convert the direct current produced by the solar PV module into suitable alternating current. Thus the sunlight is transformed in to savings and a greener planet. It is apt for the tropical countries where the sunlight is in plentiful.

Unfortunately, solar characteristics rely on ecological conditions like irradiance intensity and temperature [1]. The fickleness of solar energy transpires us to find an active method to leverage it when they are accessible. The fickleness of solar energy can be overcome using Maximum Power Point Tracking algorithm (MPPT). Perturb and Observation (P&O) MPPT algorithm can execute maximum power point for rapid change in environmental conditions such as irradiance intensity and temperature. Therefore maximum power point tracking based inverter [2] is requisite in between the solar energy system source and the load arrangement. Many methods and algorithms for tracking the maximum power from the solar energy system are available. P&O [3] and incremental conductance algorithms are commonly used for the reasons of their appropriateness and ease to realize for solar photovoltaic panel.

To resolve the problem of undulations nearby the maximum power point under steady state conditions and poor tracking competency during changeable irradiance traditional P&O algorithms, the variable step size P&O has been brought in [4], [5]. The various families of power converters have been intended to interface the renewable solar resource for different applications [6]. Owing to development of Power Electronics and Embedded System techniques, control and implementation of renewable energy systems are made promising.

The output from solar system is fed as input to boost converter. The MPPT algorithm is applied to solar PV system in order to keep the boost converter output constant [7]. The output from boost converter is taken to three phase impedance-source inverter with load arrangement first and then to grid system. The three phase impedance -source inverter is operated in closed loop control for voltage and frequency synchronization. Eventually three phase impedance-source inverter output with LC filters will produce sinusoidal output which is fed to the load first and then to grid system. Thus, a control algorithm with boost converter [8] and three phase impedance-source inverter is proposed to utilize the renewable energy resource to their maximum level. The proposed three phase impedance-source inverter can either buck or boost AC output, a scenario that could not be achieved in conventional Current Source and Voltage Source Inverters (CSI and VSI) [9], [10]. Moreover, the impedance-network of three phase impedance-source inverter comprising a pair of capacitors and inductors forms the second order filter that filters unwanted voltage sags thereby reducing the current total harmonic distortion and improving the quality of power. Consequently enhanced power quality without sag and lesser total harmonic distortion is given to load [11], [12] and [13].

Amongst the different controllers, PID controller is proposed for grid connected solar based impedance-source inverter system [14], [15]. PD and PI controller improves transient and state responses respectively. Henceforth, combination of both PD and PI controllers improves overall time response of the system. However it affects both transient as well as steady state performance of the system. For effective result it must be tuned properly. There is no offset error in this controller. It makes the system response fast and reduces the settling time.

## 2. IMPEDANCE-SOURCE INVERTER

Impedance-source inverter shown in Figure 1 has impedance-network on its DC side. The exclusive impedance network consists of passive components (Inductors and Capacitors) that give single-stage conversion.

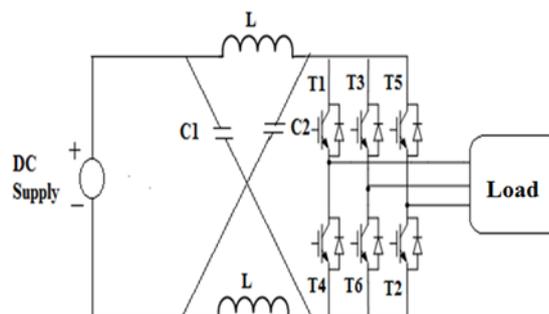


Figure 1. Schematic of impedance-source inverter

A second-order filter which handles the unwanted voltage sags in a DC voltage source is formed by the impedance network. It reduces the harmonics in the current due to dual inductors in impedance-source network as well as in rush current [16], [17]. Existence of two inductors and capacitors in impedance-source network permits both the switches in the same leg in ON state, concurrently named as “shoot through state”. This state provides boosting ability to the inverter without destructing the switching devices. In this state, energy is transmitted from capacitor to inductor and hence impedance-source network gains the voltage boosting ability. A diode is mandatory to avoid the discharge of charged capacitor through the source.

The major advantages of impedance-source inverter [18], [19] are:

- Can step down (buck) or step up (boost) voltage outputs which are not possible with voltage source and current source inverters.
- Produce any anticipated voltage outputs, more than the line voltage, irrespective of the voltage input, thus lessen the ratings of motor.
- It provides ride-through at the instance of voltage sags minus any additional circuits
- Less affected by Electro Magnetic Interference (EMI) noise and
- Improve power factor and reduces harmonic current.

**3. RESULTS AND DISCUSSION**

The Simulink models [20], [21] of the following cases are carried out by means of MATLAB software.

- a. Solar system with closed loop PID controller for RL load
- b. Solar system with closed loop PID controller for grid system.

The simulation parameters of constructing boost converter, Impedance-source inverter and controllers are tabulated below in Table 1 and Table 2. Table 3 show about specifications of controllers. Also the transient and steady state response parameter for PID controller is tabulated in 3 and simulink model consists of the following blocks:

- a. MPPT controller based solar panel
- b. Boost converter
- c. Impedance-Source Inverter
- d. RL Load and grid

**Table 1. Simulation Parameters of Converter and Inverter**

Boost converter	Impedance-filter	Impedance-source inverter
$V_{in}=40V$	$C_1, C_2=$ $3000\mu F$	$L_1, L_2, L_3=$ $800mH$
$C_1=1000 \mu F$		
$L_1=15 \mu H$		$C_1, C_2, C_3=$ $10 \mu F$
$C_s=0.6mf$	$L_1, L_2=$ $100mH$	$R=10\Omega$
$C_{out}=3000\mu F$		$L=800mH$
$V_o=80V$		

**Table 2. Simulation Parameters of Controller**

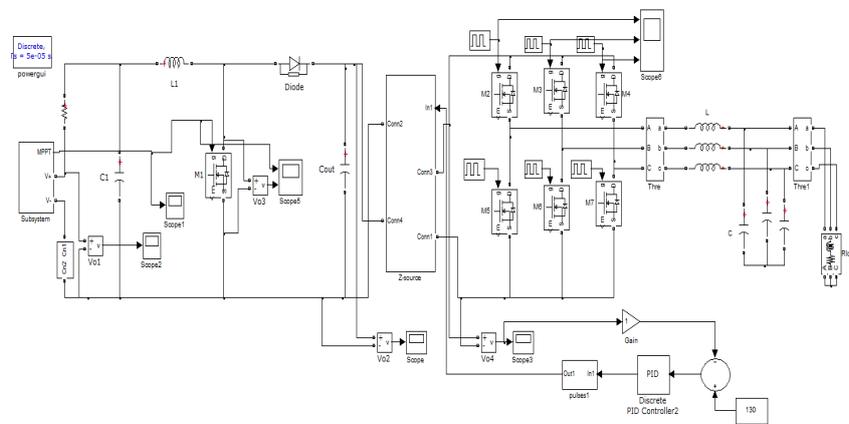
Type of Controller	$K_p$	$K_i$	$K_d$	$T_s$
PID	0.009	0.8	0.007	$50\mu s$

**Table 3 Specifications of Controllers**

Type of Controller	Transient Specifications			Steady state error
	Rise time (Sec)	Peak time (Sec)	Settling time (Sec)	
PID	1.2	2.8	2.2	5

- a. Case 1: Solar System with Closed Loop PID Controller for RL load without Grid System

The closed loop simulink model shown in Figure 2 comprises of solar model, boost converter, impedance-source inverter (impedance-filter and three-phase inverter), PID controller, RL load etc. is aimed for a switching frequency of 5 kHz and the outcomes of the same are presented below.



**Figure 2. Simulink model-solar system with closed loop PID controller for RL load without grid system**

The output voltage of 40V from solar panel is given to boost converter where it is boosted to nearly 80 V as shown in Figure 3 and Figure 4. The output voltage across connected RL load and output current through connected RL load without grid system are shown in Figure 5 and Figure 6. The FFT analysis is obtained as in Figure 7 for solar system with closed loop PID controller for RL Load without grid system. The current total harmonic distortion for PID controller is 4.6%.which is well below the standard.

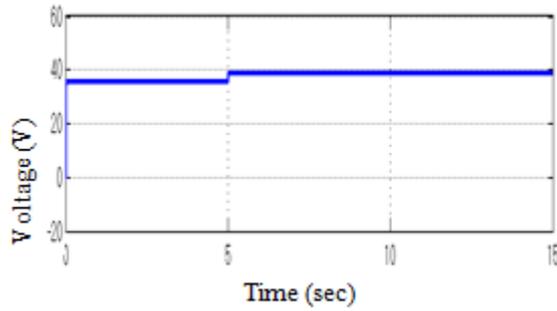


Figure 3. Input voltage of boost converter

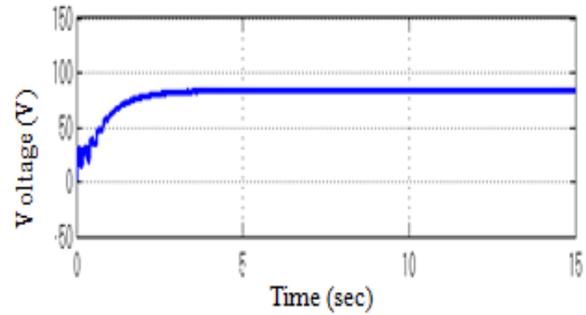


Figure 4. Output voltage of boost converter

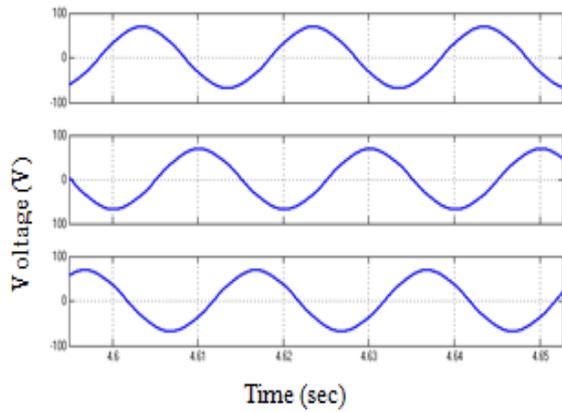


Figure 5. Output voltage across RL load without grid system

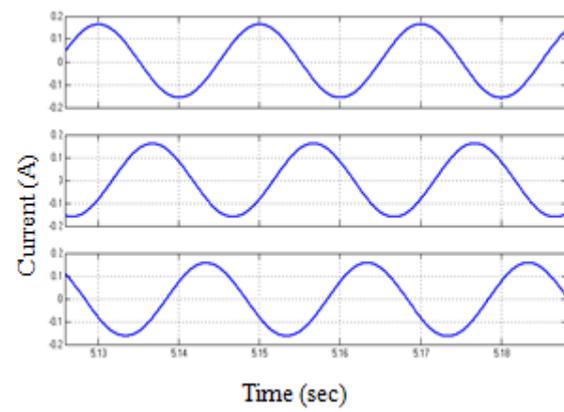


Figure 6. Output current through RL load without grid system

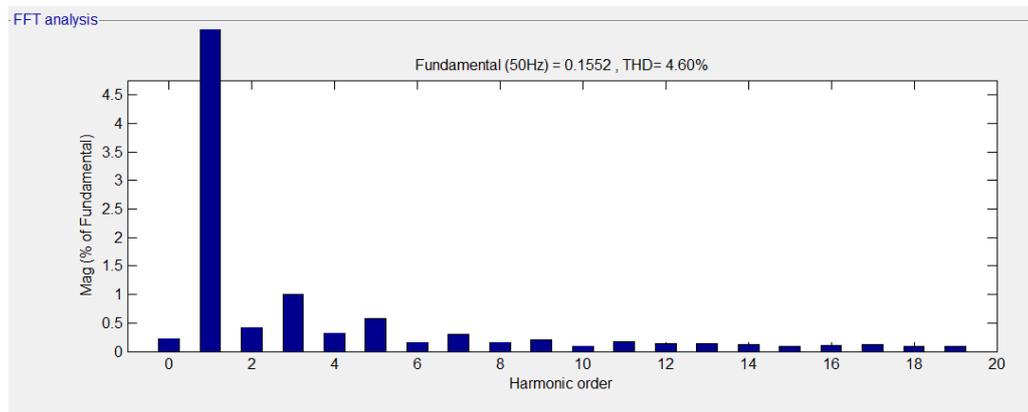


Figure 7. Analysis of current THD

b. Case 2: Solar System with Closed Loop PID Controller for Grid System

The closed loop simulink model shown in Figure 8 comprises of solar model, boost converter, impedance-source inverter (impedance-filter and three-phase inverter), PID controller, RL Load, grid system etc. is aimed for a switching frequency of 5 kHz and the outcomes of the same are presented.

The output voltage of 40V from solar panel is given to boost converter where it is boosted to nearly 80 V as shown in Figure 9 and Figure 10. The output voltage across connected grid and output current through connected grid are shown in Figure 11 and Figure 12.

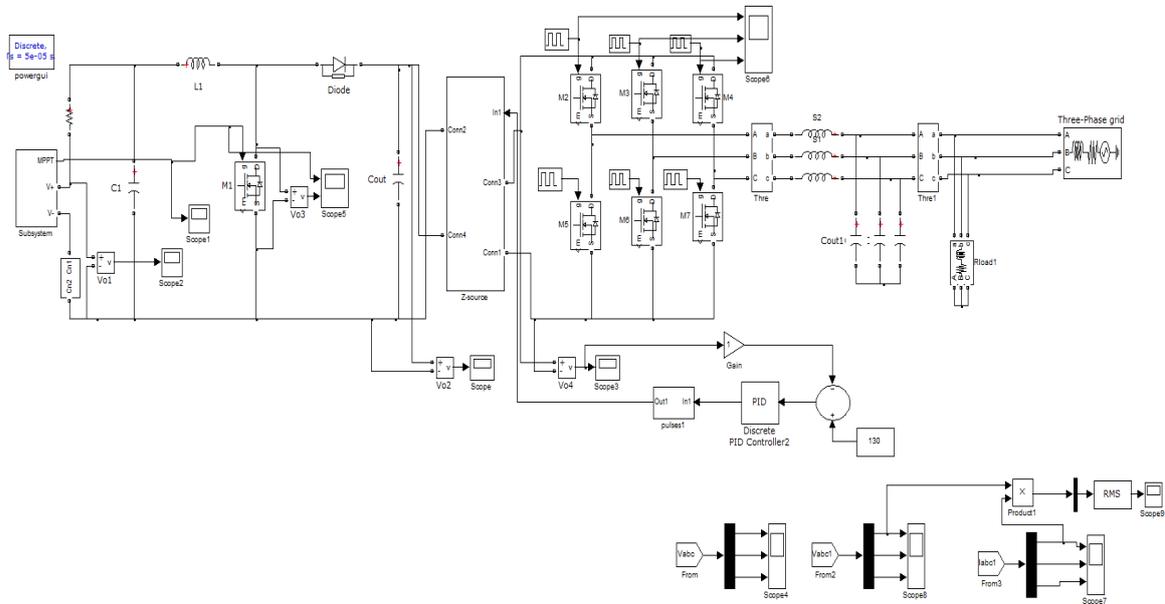


Figure 8. Simulink model–solar system with closed loop PID controller for grid system

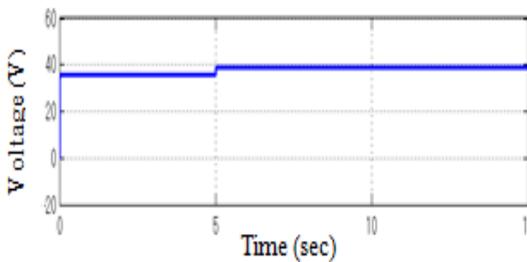


Figure 9. Input voltage of boost converter

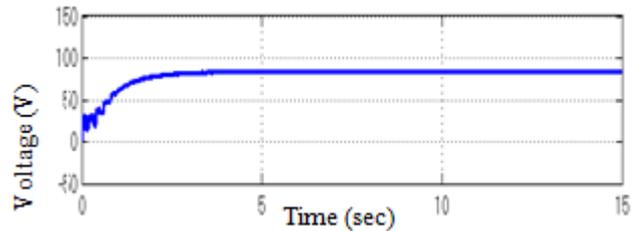


Figure 10. Output voltage of boost converter

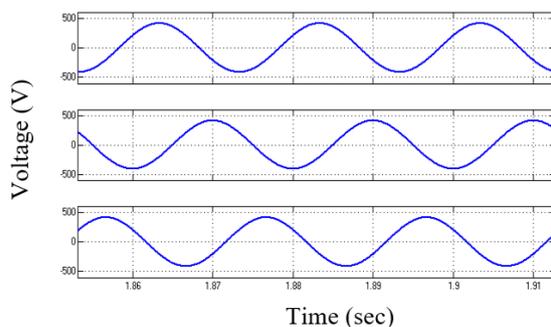


Figure 11. Output voltage across grid system

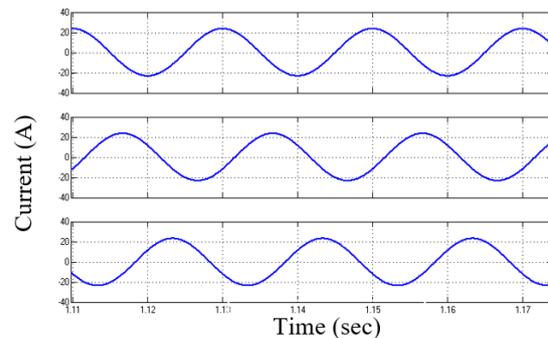


Figure 12. Output current through grid system

The FFT analysis is obtained as in Figure 13 for solar system with closed loop PID controller for grid system. The current total harmonic distortion with PID controller is 3.05% which is comparatively less.

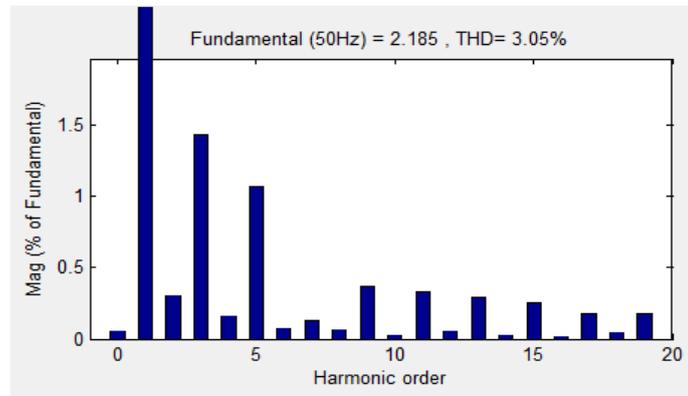


Figure 13. Analysis of current THD

The Figure 14 is prototype hardware model of impedance-source inverter for solar PV energy system. It consists of solar PV panel, boost converter; impedance -source inverter (impedance-filter and three phase inverter), control circuitry and RL load arrangement. The PIC controller circuitry is preferred for this solar PV energy system for the creation of all control signals to boost converter and impedance-source inverter. These signals are amplified by driver ICs. The input voltage required both by driver and controller circuitries are provided by voltage regulator. The output voltage across RL load is shown in Figure 15.



Figure 14. Prototype hardware model–impedance-source inverter for solar energy system

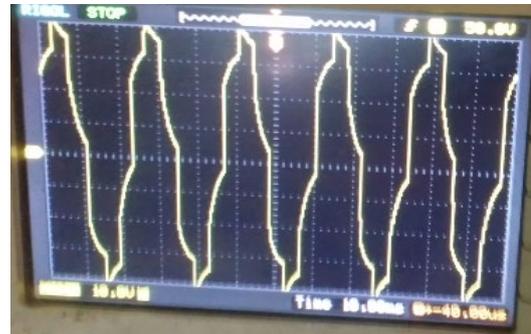


Figure 15. Output voltage across RL load

#### 4. CONCLUSION

A solar based impedance-source inverter with PID controllers for both RL load without grid and with grid system are analysed and compared. The accomplishment demonstrates that the performance of the system with grid was better with regard to that of RL load without grid system. Digital simulation and prototype hardware modelling of solar based impedance-source inverter with PID controllers is established and the output obtained under transient and steady state conditions. The future enhancement of this work is to simulate for wind energy system and to realize a hardware model of the system proposed.

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