Carrier-Based Pwm Technique for Inverter-Fed Multiphase Induction Motor

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ABSTRACT

Article history: Received Feb 20, 2016 Revised Apr 5, 2016 Accepted Apr 25, 2016	Multiphase (more than three phases) is very much popular due to their eminent features compared to conventional three-phase counter parts. In order to drive the multiphase machine, it requires same phase input w.r.t the no of phases at the output. This paper mainly focuses on five phase, because even after failure of one phase, the performance does not degraded much. Voltage source inverters (VSIs) are used to feed the induction motor. Voltage source inverters (VSIs) switches are ON and OFF precisely to control the output. In order to implement harmonic waveform characteristic, carrier based PWM (pulse width modulation) is performed. By using with and without third harmonic injection machine torque is highly improved. Using MATLAB software, the simulation results are presented in the form of computer traces and high traded performance of the machine are discussed.
<i>Keyword:</i> Carrier-Based pulse width modulation (PWM) Universal voltage source inverters (VSIs)	

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1. INTRODUCTION

It is considered as the workhorse for the industry. Induction machines has almost replaced the D.C machine in the industry due to the simplicity of the design, low-cost, low maintenance cost, ruggedness and direct connection to A.C power source compared to D.C. maximum total power consumption in the industrial sector is from "induction motors" only. Induction motor fed with voltage source inverter, the magnitude of stator current is determined by means of leakage inductance of the motor and source amplitude at harmonic frequencies. Hence it needs to diminish the magnitude along with rise of frequency of torque oscillations [1] also reduces the rotor harmonic current losses, dc link current harmonics by increasing the number of phases of the machine drive. Raising the number of phases then it is feasible to amplify the torque per rms ampere for the same machine output. Five-phase Induction motor acquire dominance features over three phase motor drives such as using existing low voltage power devices to realize high power drives. Applications for multiphase induction machine are mainly concerned on aircrafts, electric ship propulsion, and hybrid electric vehicles (HEV) etc.

2. BLOCK DIAGRAM FOR MULTI PHASE INDUCTION MOTOR

Multiphase induction motor feed by a voltage source inverter. As shown in Figure 1, VSI dc link voltage is varied from the controlled rectifier. The output of the inverter is fed to five phase induction motor. Rectifier converts ac to dc. Here the pulsating dc is obtained from the rectifier, it contains harmonics. These harmonics are eliminated by using filters. Hence the input of the five phase inverter is pure dc.



Figure 1. Five phase VSI fed Induction Motor

Multi phase motor drive diminishes current ripple, unfailing under inadequate condition, and reduction in phase current.

3. TWO LEVEL FIVE LEG VOLTAGE SOURCE INVERTER

Here, the upper switch and lower switch are termed as leg because to understand the inverted action very easily such as to be termed as upper switch or upper leg and lower switch or lower leg (Figure 2). The phase delay between each leg is 72° where as phase delay between the consecutive switches is 360. The below schematic diagram consists of fully controllable five phase voltage source inverter. VSI is operated in ten modes ,in each mode five switches are in ON state, two switches from the upper group and three switches from lower group or vice versa. In order to avoid the short circuiting in any leg of the inverter no two switches are turned on (Figure 3).



Figure 2. Two Level Five Leg voltage source inverter



Figure 3. Representation of dead line and how power switch is working in-terms of plotted waveform

4. MATHEMATICAL REPRESENTATION AND ANALYSIS OF MULTI PHASE INDUCTION MOTOR

The operation of five leg inverter switches is in two states. Each leg consists of two switches. When one switch is ON state and the other switch is in OFF state. In Figure 2, the Upper leg switches are represented as S_{ap} , S_{bp} , S_{cp} , S_{dp} , S_{ep} , where as lower leg switches are S_{an} , S_{bn} , S_{cn} , S_{dn} , S_{en} . where First subscript refers to corresponding phase and second subscript refers to upper leg or lower leg switch (p-upper leg, n-lower leg). When switch in ON, it takes the value of unity and in OFF state it takes the value of zero. The generalised switching device can be represented as S_{ij} . Where "i" represents the corresponding phase which it is to be associated and "j" represents the upper and lower device of the particular leg of the inverter. Five leg inverter output voltage equations are expressed in terms of input dc voltage V_d are as follows.

$$v_{io} = \frac{v_d}{2} (2\text{Sip-1}) = v_{in} + v_{no,i} = a, b, c, d, e$$

$$v_{ao} = \frac{v_d}{2} (\text{sap-san}) = \frac{v_d}{2} (\text{sap} - (1 - \text{sap}))$$

$$= \frac{v_d}{2} (\text{sap} - 1 + \text{sap})$$

$$= \frac{v_d}{2} (2S_{ap} - 1)$$

$$= v_{an} + v_{no}$$
(2)

similarly,

$$v_{bo} = \frac{v_d}{2} \left(2S_{bp} - 1 \right) = v_{bn} + v_{no} \tag{3}$$

$$v_{co} = \frac{v_d}{2} \left(2S_{cp} - 1 \right) = v_{cn} + v_{no} \tag{4}$$

$$v_{do} = \frac{v_d}{2} \left(2S_{dp} - 1 \right) = v_{dn} + v_{no} \tag{5}$$

$$v_{eo} = \frac{v_d}{2} \left(2S_{ep} - 1 \right) = v_{en} + v_{no} \tag{6}$$

Now, equating L.H.S and R.H.S terms, Hence, we get

 $v_{ao} + v_{bo} + v_{co} + v_{do} + v_{eo} = v_{an} + v_{bn} + v_{cn} + v_{dn} + v_{en} + v_{no} + v_{no} + v_{no} + v_{no}$

Therefore,

$$v_{ao} + v_{bo} + v_{co} + v_{do} + v_{eo} = v_{an} + v_{bn} + v_{cn} + v_{dn} + v_{en} + 5v_{no}$$
(7) where,

$$v_{ao} + v_{bo} + v_{co} + v_{do} + v_{eo} = \frac{v_d}{2} \left(2S_{ap} - 1 \right) + \frac{v_d}{2}$$

$$\left(2S_{bp} - 1 \right) + \frac{v_d}{2} \left(2S_{cp} - 1 \right) + \frac{v_d}{2} \left(2S_{dp} - 1 \right) + \frac{v_d}{2} \left(2S_{ep} - 1 \right)$$
(8)

Now, balanced five -phase system, and eliminate the higher order terms (or) represented as simply zero. Then

$$v_{an} + v_{bn} + v_{cn} + v_{dn} + v_{en=0} \tag{9}$$

Substitute equation 9 in equation 8, then

$$v_{ao} + v_{bo} + v_{co} + v_{do} + v_{eo} = 5v_{no} \tag{10}$$

Substitute eq.8, in eq.10 then,

$$5v_{no} = \frac{v_d}{2} \left(2S_{ap} - 1\right) + \frac{v_d}{2} \left(2S_{bp} - 1\right) + \frac{v_d}{2} \left(2S_{cp} - 1\right) + \frac{v_d}{2} \left(2S_{dp} - 1\right) + \frac{v_d}{2} \left(2S_{ep} - 1\right)$$
(11)

 v_{an} , v_{bn} , v_{cn} , v_{dn} , v_{en} Indicates the phase voltages w.r.to the load and v_{no} refers voltage w.r.t the load.

5. PULSE WIDTH MODULATION (PWM) USING INVERTER FED MULTI PHASE INDUCTION MOTOR

According to the switches ON and OFF of the inverter, pulses are generated. Consider the magnitude of the pulses either unity or Zero depends upon the switching action. From the fourier analysis the expressions are written as follows.

$$S_{ip} = 0.5(1 + M_{ip})$$
(12)

$$2S_{ip} = (1 + M_{ip}) 2S_{ip} - 1 = M_{ip}$$
(13)

$$M_{ip} = \frac{2(v_{in} + v_{n0})}{v_d}, i = a, b, c, d, e$$
(14)

6. ZERO- SEQUENCE SIGNAL UNBIASED VOLTAGE

From equation 2 to 6, neutral voltage v_{n0} , can be written as

$$\frac{v_d}{2}(2S_{ip} - 1) = v_{io} \tag{14}$$

$$v_{no} = \frac{v_d}{2} \left(2S_{ip} - 1 \right) - v_{in} \tag{15}$$

$$\frac{2v_{no}}{v_d} = 2S_{ip} - 1 - \frac{2v_{in}}{v_d}$$
(16)

$$2S_{ip} - 1 = \frac{2v_{in}}{v_d} + \frac{2v_{no}}{v_d}$$
(17)

$$2S_{ip} - 1 = m_{ipp} + m_o \tag{18}$$

Where, $m_{ipp} = \frac{2v_{in}}{v_d}$, $m_o = \frac{2v_{no}}{v_d}$ and i = a, b, c, d, e. The upper switch is turned ON in VSI

$$m_o = 1 - m_{ipp} \tag{19}$$

When the upper switch is turned off in VSI, then

$$m_o = -1 - m_{ipp} \tag{20}$$

There are six feasible solutions for Mo from eq. (19) and eq. (20) since the equation is determined and yielding a infinite number of possibilities. However, Mo lies between 1 and – 1 since the normalized high-frequency triangular signal also lies within this range [1]. The appropriate solution has to be obtained from equation (19) and (20). With the following definitions,

$$M_{max} = Max(m_{ipp}) \quad M_{min} = Min(m_{ipp})$$
⁽²¹⁾

Here m_{ipp} consists of two values i.e Max and Min

The extrema of the m_0 are $(1 - M_{\text{max}})$, $(-1 - M_{\text{min}})$, $(1 - M_{\text{min}})$ and $(-1 - M_{\text{max}})$ of which have the magnitudes between the range (1, -1). The two solutions are now linearly dependent to determine the average value of m_0 , where $0 \le \alpha \le 1$ and is given by [2]

$$\langle m_0 \rangle = \alpha (-1 - M_{min}) + (1 - \alpha)(1 - M_{max})$$
 (22)

$$\langle m_0 \rangle = (1 - 2\alpha) + (\alpha - 1)(M_{max}) - \alpha M_{min}$$
⁽²³⁾

Here the No. of carrier pulse width modulation signals are provided by the value of α .

By changing δ , different discontinues modulation signals are obtained. Figure 4 through Figure 7 has defined the modulation signals and corresponding phase 'a' load voltage at different values of α and modulation signals δ [2]. In this result the peak fundamental voltage is 150V, and third harmonic voltage is 15V. The D.C voltage is 300V. Figure 4 through Figure 9 the result only having the fundamental component is considered, whereas Figure 10 through Figure 24 shows the result having the fundamental voltage and third harmonic voltage has been considered. When α =0.5 carrier based modulation scheme for known space vector modulation scheme where null states are equally weighed.



Figure 4. Five phase modulation signals for $\alpha = 0.5$



Figure 5. Phase 'a' load voltage, $\alpha = 0.5$



Figure 6. Five phase modulation signals for, $\delta = -36^{\circ}$

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Figure 7. Phase 'a' load voltage $\delta = -36^{\circ}$



Figure 8. Five phase modulation signals for, $\delta = 0^0$









Figure 11. Five phase modulation signals for, $\delta = -36^{\circ}$, $V_{m3} = +15v$



Figure 12. Phase 'a' load voltage $\delta = -36^{\circ}$, $V_{m3} = +15v$



Figure 13. Phase 'a' fundamental and reference voltage, $\delta = 0^0$, $V_{m3} = +15v$



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Figure 15. Phase 'a' load voltage $\delta = 0^0$, $v_{m3} = +15v$



Figure 16. Phase 'a' fundamental and reference voltage, $\alpha = 0.5$, $V_{m3} = +15v$



Figure 17. Five phase modulation signals for, $\alpha = 0.5$, $V_{m3} = +15v$



Figure 18. Phase 'a' load voltage $\alpha = 0.5$, $v_{m3} = +15v$











Figure 21. Phase 'a' load voltage $\alpha = 0.5, V_{m3} = -15v$





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Figure 23. Five phase modulation signals for, $\delta = 0^0$, $V_{m3} = -15v$



Figure 24. Phase 'a' load voltage $\delta = 0^0$, $V_{m3} = -15v$

7. SIMULATION RESULTS

For, five phase induction machine is simulated by three sets of connections for below Figure 25(a), is assumed as star-connected the phase voltage across the machine windings are v_{as} , v_{bs} , v_{cs} , v_{ds} , v_{es} . It is assumed to be conventionally delta connected the phase voltage across the machine windings are v_{as} , v_{bs} , v_{cs} , v_{cs} , v_{cs} , v_{cs} , v_{cs} , v_{cs} , v_{es} , v_{e

Conventional Star Connection:



Figure 25(a)

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The phase voltage is given by $Vas = vmcos(\theta e)$ $Vbs = vmcos(\theta e - 2\pi/5)$ $Vcs = vmcos(\theta e - 4\pi/5)$ $Vds = vmcos(\theta e - 6\pi/5)$ $Ves = vmcos(\theta e - 8\pi/5)$

line-line voltage is given by $Vab=1.1756vmcos(\theta e+3\pi/10)$ $Vbc=1.1756vmcos(\theta e-\pi/10)$ $Vcd=1.1756vmcos(\theta e-5\pi/10)$ $Vde=1.1756vmcos(\theta e-9\pi/10)$ $Vea=1.1756vmcos(\theta e+7\pi/10)$

line and phase currents are same $ia = Im \cos(\Theta e + \delta)$ $ib = Im \cos(\Theta e + \delta - 2\pi/5)$ $ic = Im \cos(\Theta e + \delta - 4\pi/5)$ $id = Im \cos(\Theta e + \delta - 6\pi/5)$,

 $ie = Im \cos(\Theta e + \delta - 8\pi/5)$

Conventional Delta Connection:



Figure 25(b)

phase and line-to-line voltages are same are given by. $Vab=Vas=1.1756vmcos(\Theta + 3\pi/10)$ $Vbc=Vbs=1.1756vmcos(\Theta - \pi/10)$ $Vcd=Vcs=1.1756vmcos(\Theta - 5\pi/10)$ $Vde=Vds=1.1756vmcos(\Theta - 9\pi/10)$, $Vcd=Vcs=1.1756vmcos(\Theta + 7\pi/10)$.

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phase currents are given by

iab=1.1756Imcos(\Theta + \delta + 3\pi/10)

ibc=1.1756Imcos(\Theta + \delta - \pi/10)

icd=1.1756Imcos(\Theta + \delta - 5\pi/10)

ide=1.1756Imcos(\Theta + \delta - 9\pi/10)

iea=1.1756Imcos(\Theta + \delta + 7\pi/10).
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line currents are given by $ia=1.3820Imcos(\Theta e + \delta)$ $ib=1.3820Imcos(\Theta e + \delta - 4\pi/10)$ $ic=1.3820Imcos(\Theta e + \delta - 8\pi/10)$ $id=1.3820Imcos(\Theta e + \delta + 8\pi/10)$ $ie=1.3820Imcos(\Theta e + \delta + 4\pi/10)$

7.1. Without Third Harmonic Voltage Injection

The fundamental frequency of inverter fed induction motor with a basic voltage is 60 HZ. Figure 26 through 33 shows different results from machine windings. Whereas, Figure 26 through Figure 29 shows normal operation without using inverter, whereas, Figure 30 through Figure 33 results when a five phase machine is supplied through inverter. Figure 26, 27, 30 and 31 the machine connection is star connection. Figure 28, 29, 32 and 33 the stator winding is delta connection (conventionally). The maximum value of the elemental voltage maintained is 187.79V and D.C voltage is 360V. In all the results I_{as} , V_{as} , T_e , I_{ar} and speed are shown as follows.





Figure 26. Stator Starting Transients - For Star Connection



Figure 27. Load Transients on Stator side - Star Connection







Figure 29. Load transients on stator side - Delta Connection



SIMULATION RESULTS OF INDUCTION MOTOR USING INVERTER



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Figure 31. Load transients on stator side - Star Connection



Figure 32. Stator Starting transients - Delta Connection



Figure 33. Load transients on stator side - Delta Connection

7.2. Injection of Third Harmonic Supply Voltage Component

The third harmonic voltage with the 10% magnitude of the fundamental was added to the fundamental voltage. The simulation analyzes the effect of this amount if at all there is any significant in the overall torque caused by third harmonic component. Figure 34 through 37 show normal operation without an inverter, whereas Figure 38 through Figure 41 results when a five phase machine is supplied through inverter. Figure 34, 35, 38, 39, shows the machine winding is star connected; While Figure 36, 37, 40, 41, shows in delta form. (conventionally). In all the simulation results the torque can be represented as fundamental, third, total torque. Peak amount of the fundamental voltage used in this simulation is 187.79 V and that of the third harmonic component is 18.779 V. The dc voltage is 360 V.

SIMULATION RESULTS OF INDUCTION MOTOR WITHOUT USING INVERTER







Figure 35. Load transients on stator side - Star Connection



Figure 36. Stator Starting transients - Delta Connection



Figure 37. Load transients on stator side - Delta Connection



SIMULATION RESULTS OF INDUCTION MOTOR USING INVERTER

Figure 38. Stator Starting transients - Star Connection



Figure 39. Load transients on stator side - Star Connection



Figure 40. Stator Starting transients - Delta Connection



Figure 41. Load transients on stator side - Delta Connection

8. CONCLUSION

The five-phase carrier based pulse width modulation inverter schematic has to be analysed. It is used in supplying the Inverter fed five phase induction machine. Both, the basic and harmonic components have considered. Hence, harmonic voltage of the 10% magnitude of the elemental voltage has been injected. Hence, the outcome of this injected voltage as shown in the results. The third harmonic torque component has a finite value, although not to contributing the much to require the total torque. Also, three different stator windings has to be considered that is star based connection and conventional delta based connection. For, the delta connections two peak values of the peak phase voltage have been considered. The ratio of the phase voltage magnitude delta mode to star mode is 1.1756. This means more voltage can be obtained in the machine windings without a need of increasing the supply voltage.

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