

Cover letter

February 20, 2021

Prof. nzw. dr hab. inz. Lech M. Grzesiak

Editor-in-Chief

IJECE

Dear Prof. nzw. dr hab. inz. Lech M. Grzesiak,

I am pleased to submit an original research manuscript for consideration for publication in your esteemed journal “**International Journal of Electrical and Computer Engineering**” (IJECE). The manuscript is entitled “A Novel Design of Low Noise Charge Amplifiers Using 45-nm CMOS Technology”.

In this manuscript, we propose a novel design of low-noise and low-power charge amplifier that provides a new technology trend and move towards a sub 100 nm scale. We aim to build a preamplifier using 45 Nanometer technology which is made for the first time in such circuits. Our proposed design is more efficient in terms of limiting noise effect, enhancing image quality, and reducing power consumption as shown in simulation results.

It worth mentioning that this manuscript has not been published elsewhere and it has not been submitted simultaneously for publication elsewhere. Also, we have no conflicts of interest to disclose.

Thank you very much for your consideration.

Yours Sincerely,

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